
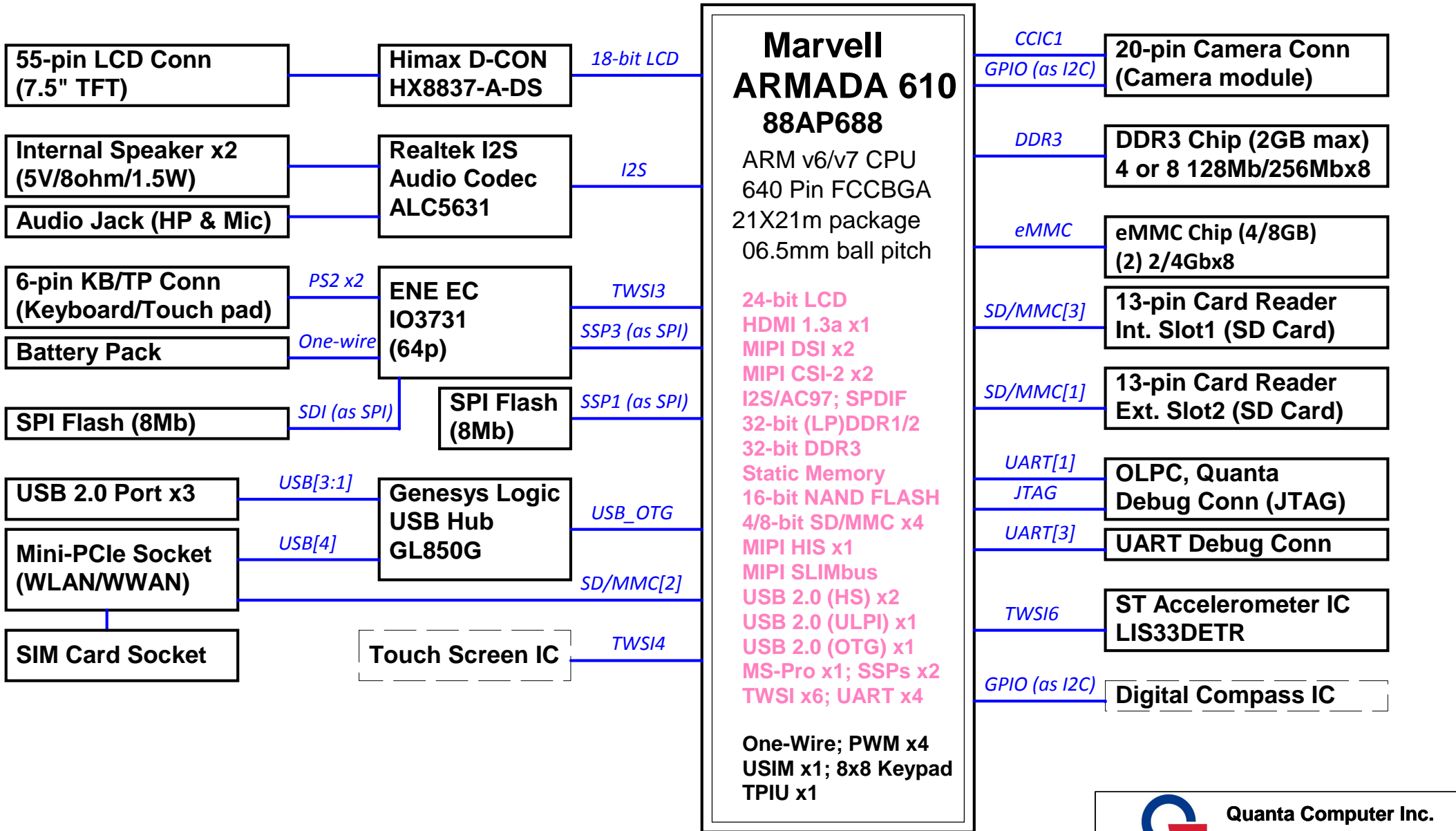


CL2 Preliminary Schematic (F Version, Ramp stage)

PAGE	DESCRIPTION	PAGE	DESCRIPTION
1	TITLE PAGE	19	EC IO3731
2	SYSTEM BLOCK DIAGRAM	20	WLAN MODULE (SDIO)
3	POWER SEQUENCE	21	USB HUB & PORTS
4	RTC BATTERY & RTC CLOCK	22	CAMERA & G-SENSOR & TOUCH
5	ARMADA 610 (1/5) GPIO	23	TPD/KBD/LED/SENSOR/BUTTON
6	ARMADA 610 (2/5) INTERFACES	24	POWER MAP
7	ARMADA 610 (3/5) DDR3 & NAND	25	PWR (1/6) CHARGER
8	ARMADA 610 (4/5) PWR	26	PWR (2/6) +3.3VSUS/+5V/+3.3V
9	ARMADA 610 (5/5) VSS & VCORE	27	PWR (3/6) +1.8V/+VCORE
10	DDR3 SDRAM	28	PWR (4/6) DDR3 PWR/+1.2V
11	DDR3 TERMINATIONS	29	PWR (5/6) LED BACKLIGHT
12	RESET CIRCUIT/HOLES	30	PWR (6/6) DISCHARGE
13	DCON HX8837	31	POWER SEQUENCE TIMING
14	LCD CONNECTOR	32	Schematic modify history A2
15	I2S AUDIO CODEC ALC5631Q	33	Schematic modify history B1
16	AUDIO JACKS	34	Schematic modify history C
17	Int. SD SLOTS1 & Ext. SD SLOTS2	35	Schematic modify history D
18	eMMC FLASH (MLC)		

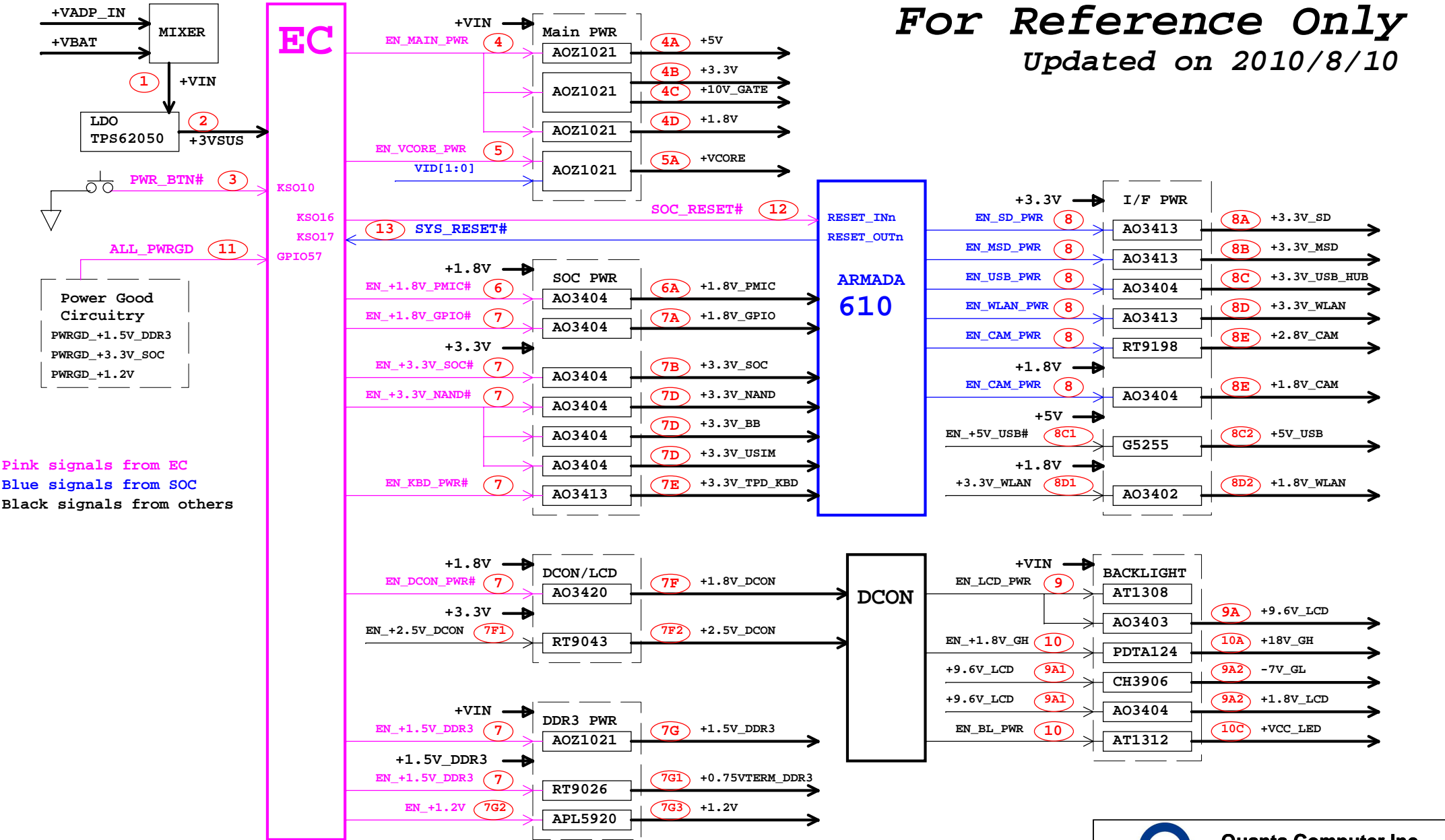
 Quanta Computer Inc. PROJECT : CL2		Rev 4A
Size	Document Number	Date: Monday, December 12, 2011
Sheet 1 of 35		


CL2 System Block Diagram (F Version, RAMP stage)



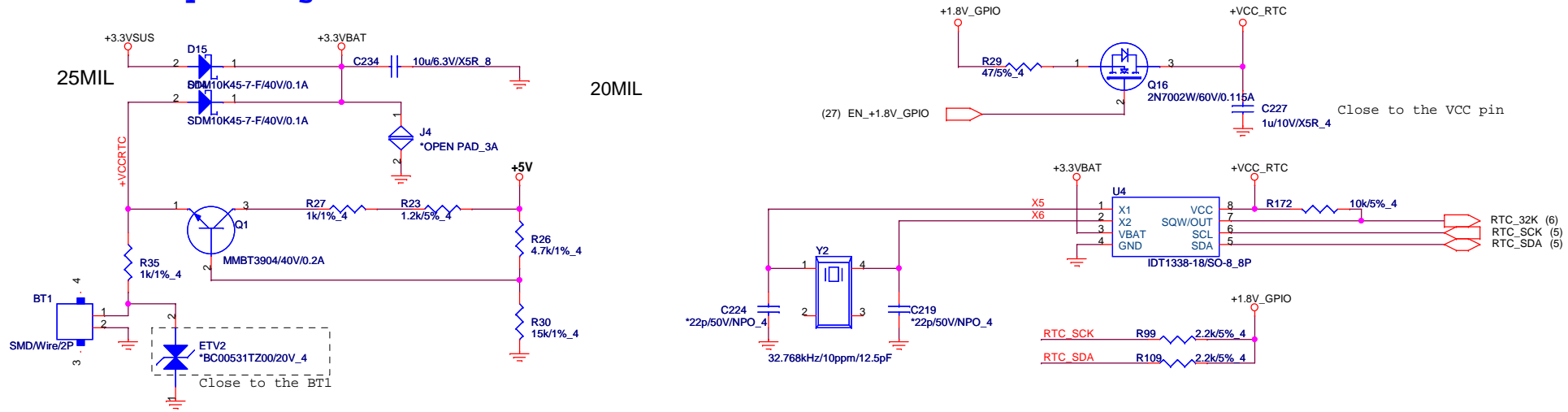
For Reference Only


Updated on 2010/8/10

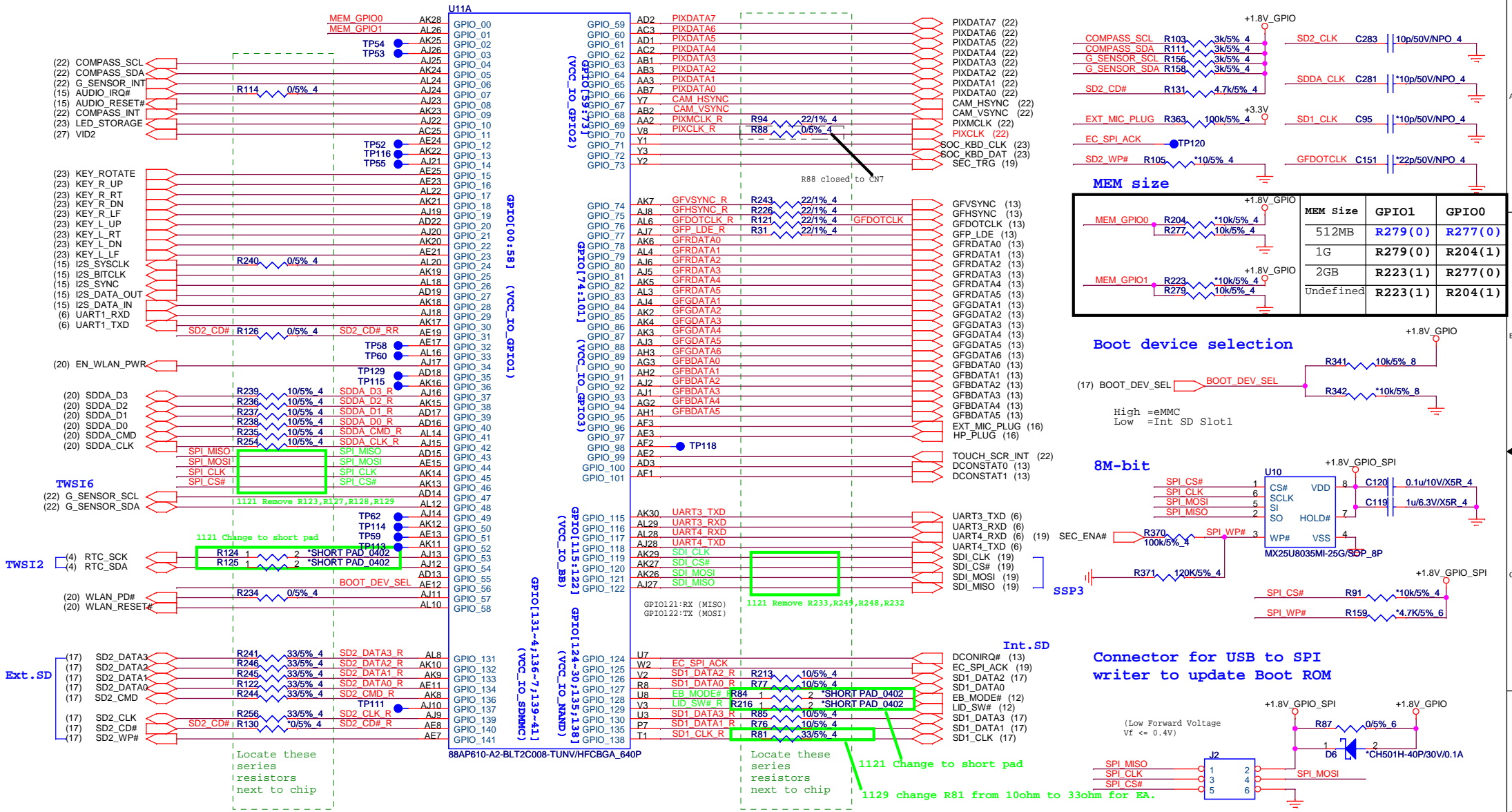


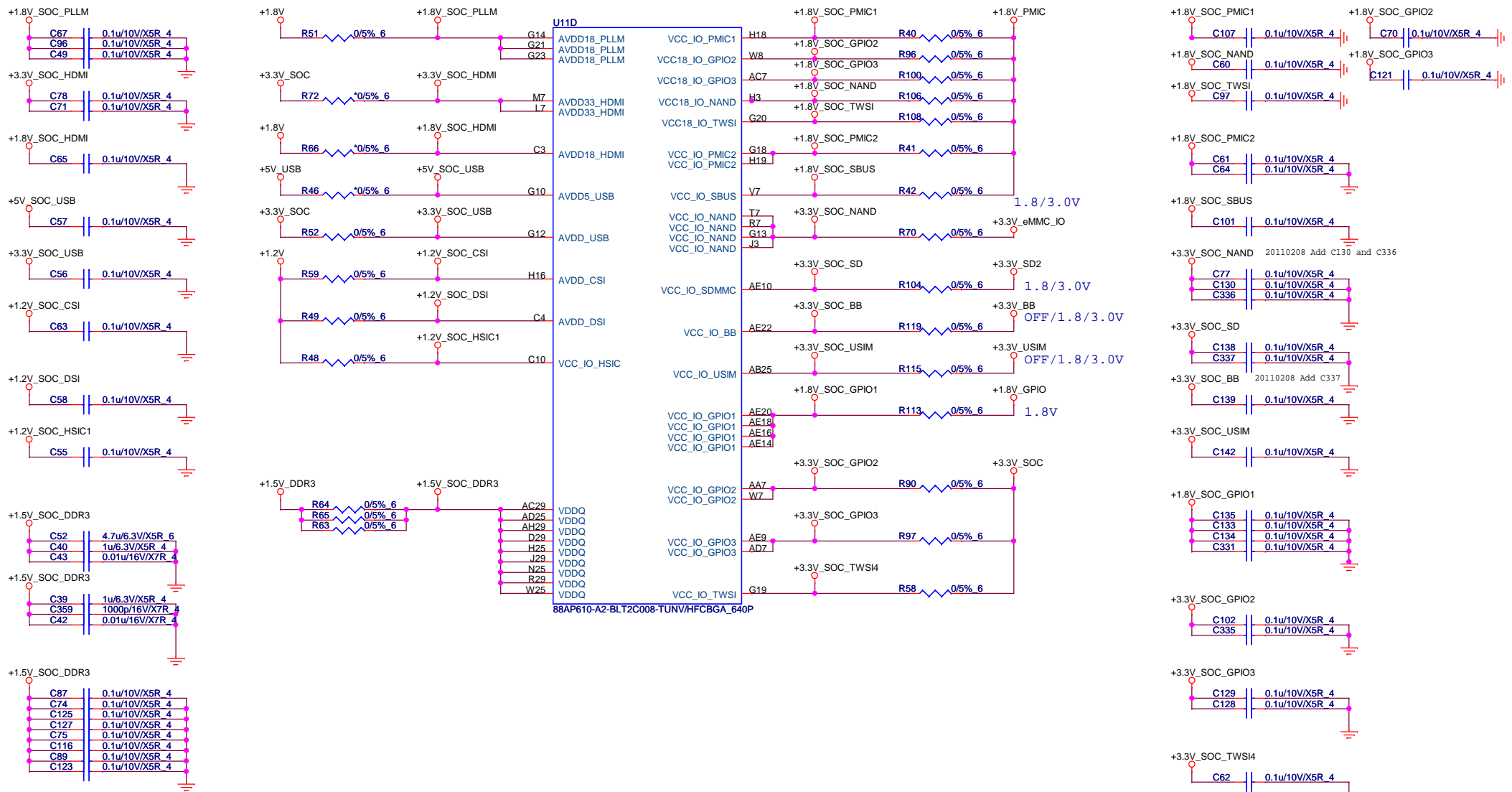
 Quanta Computer Inc. PROJECT : CL2		Rev 4A
POWER SEQUENCE		
Date:	Monday, December 12, 2011	Sheet 3 of 35

RTC Battery Charger




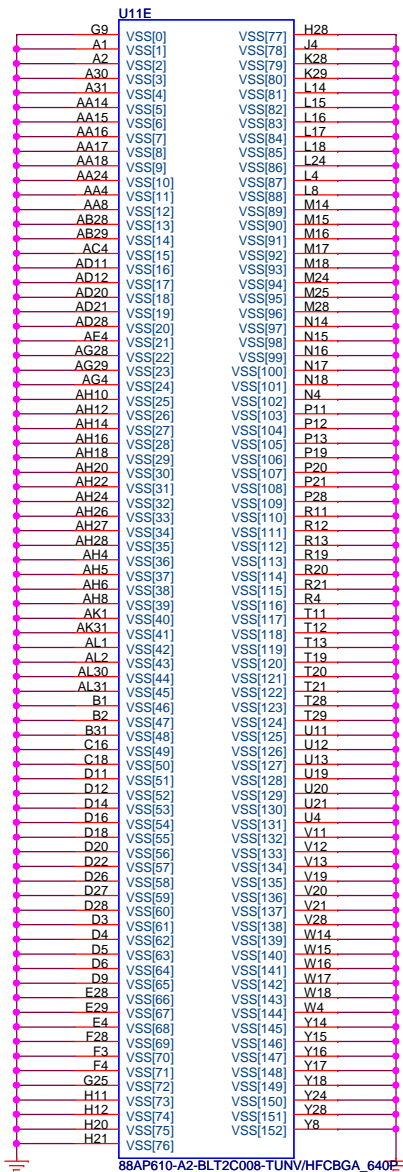
 Quanta Computer Inc. PROJECT : CL2		Rev 4A
RTC BATTERY & RTC CLOCK		
Date:	Monday, December 12, 2011	Sheet 4 of 35



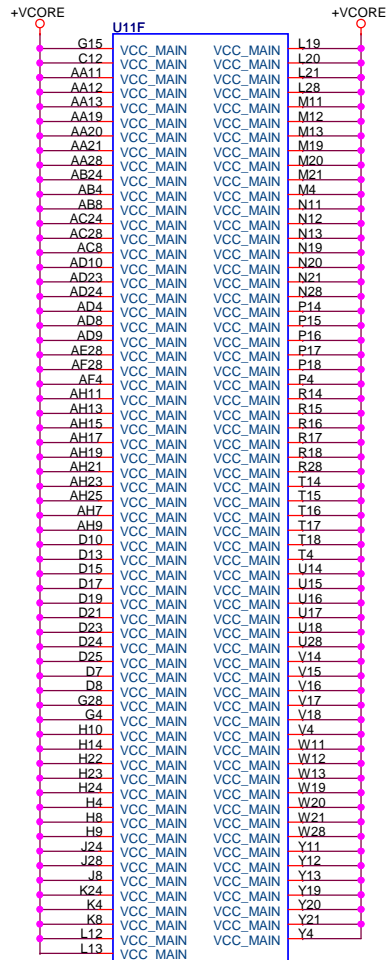


C87 --> U11.AC29
 C74 --> U11.D29
 C125 --> U11.H25,U11.J29
 C127 --> U11.AH29
 C75 --> U11.R29
 C116 --> U11.AD25
 C89 -->
 C123 -->U11.N25

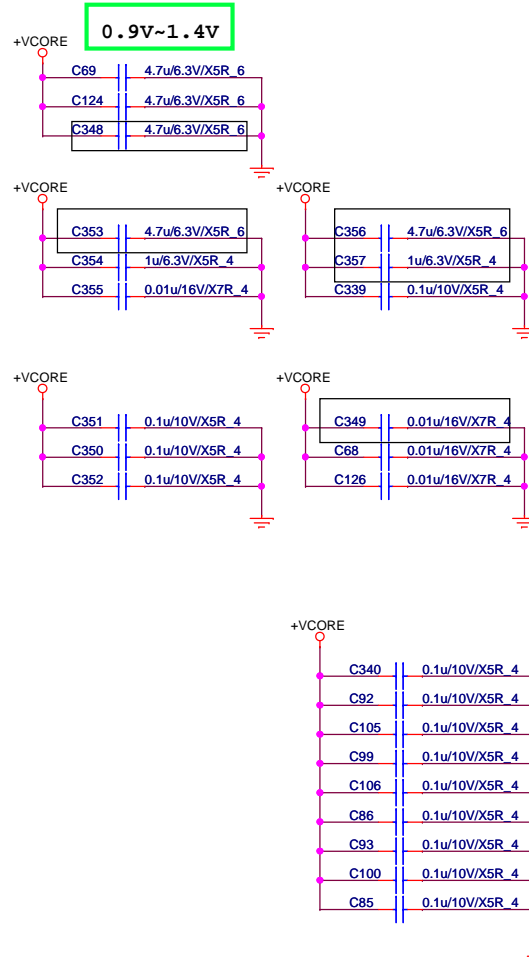
 Quanta Computer Inc. PROJECT : CL2		Size	Document Number	Rev
				4A
ARMADA 610 (4/5) PWR				
Date:	Monday, December 12, 2011	Sheet	8	of 35




88AP610-A2-BLT2C008-TUNV/HFCBGA_640P



88AP610-A2-BLT2C008-TUNV/HFCBGA_640P

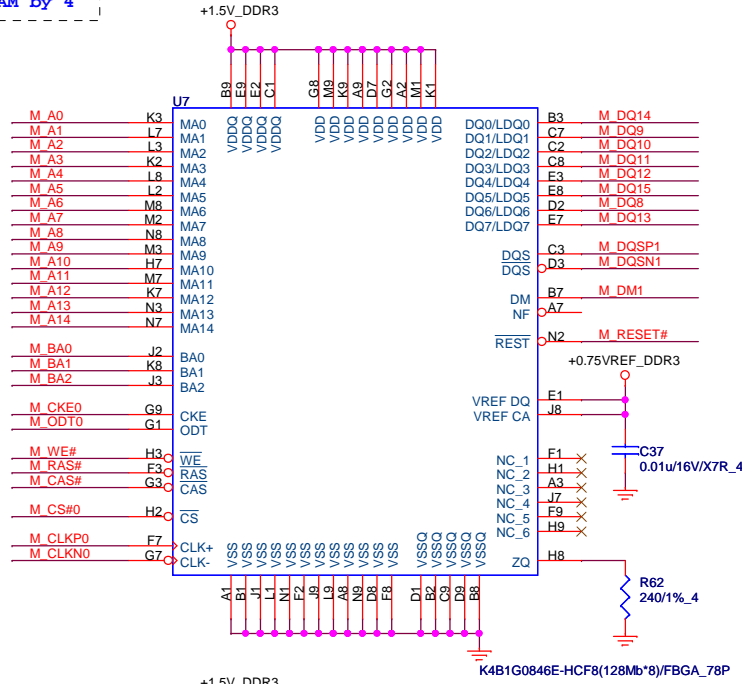
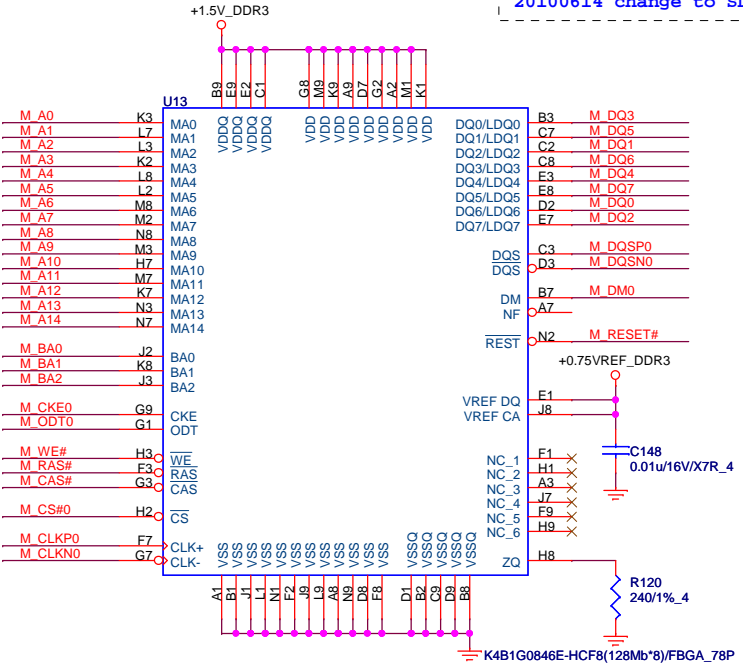




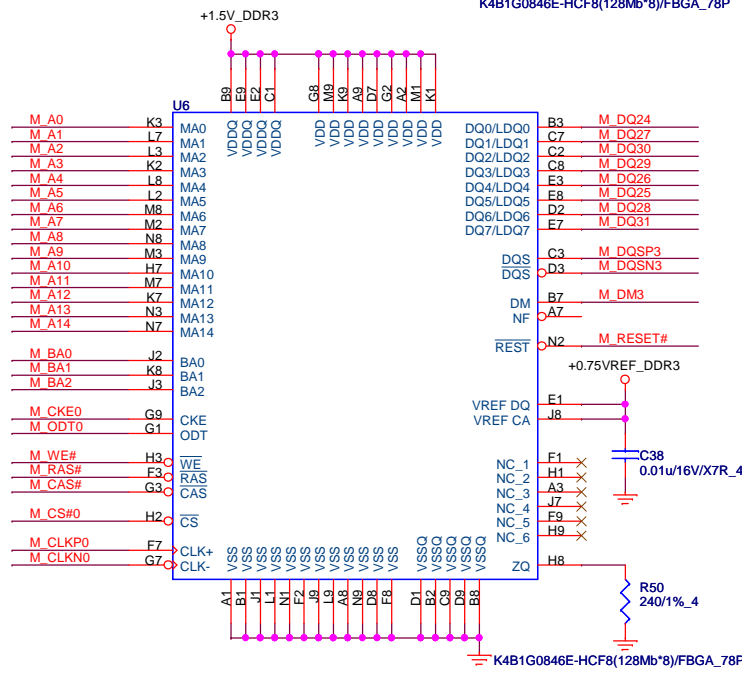
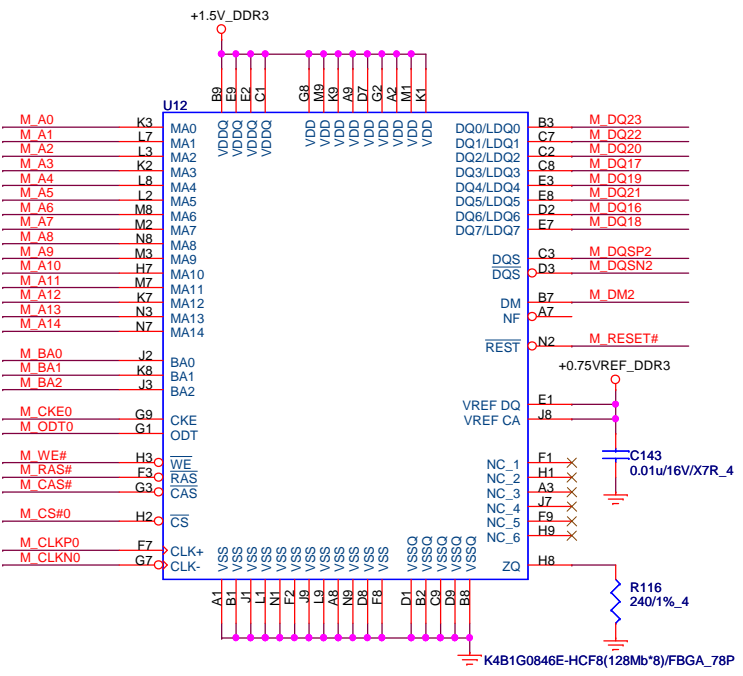
Quanta Computer Inc.
PROJECT : CL2

Size	Document Number	Rev
	ARMADA 610 (5/5) VSS & +VCCORE	4A
Date:	Monday, December 12, 2011	Sheet 9 of 35

20100614 change to SDRAM by 4



- (7,11) M_A[0:15] M_A[0:15]
- (7,11) M_BA[0:2] M_BA[0:2]
- (7,11) M_CKE0 M_CKE0
- (7,11) M_ODT0 M_ODT0
- (7,11) M_WE# M_WE#
- (7,11) M_RAS# M_RAS#
- (7,11) M_CAS# M_CAS#
- (7,11) M_CS#0 M_CS#0
- (7) M_CLKP0 M_CLKP0
- (7) M_CLKN0 M_CLKN0
- (7) M_DQ[0:31] M_DQ[0:31]
- (7) M_DQSP[0:3] M_DQSP[0:3]
- (7) M_DQSN[0:3] M_DQSN[0:3]
- (7) M_DM[0:3] M_DM[0:3]
- (7) M_RESET# M_RESET#

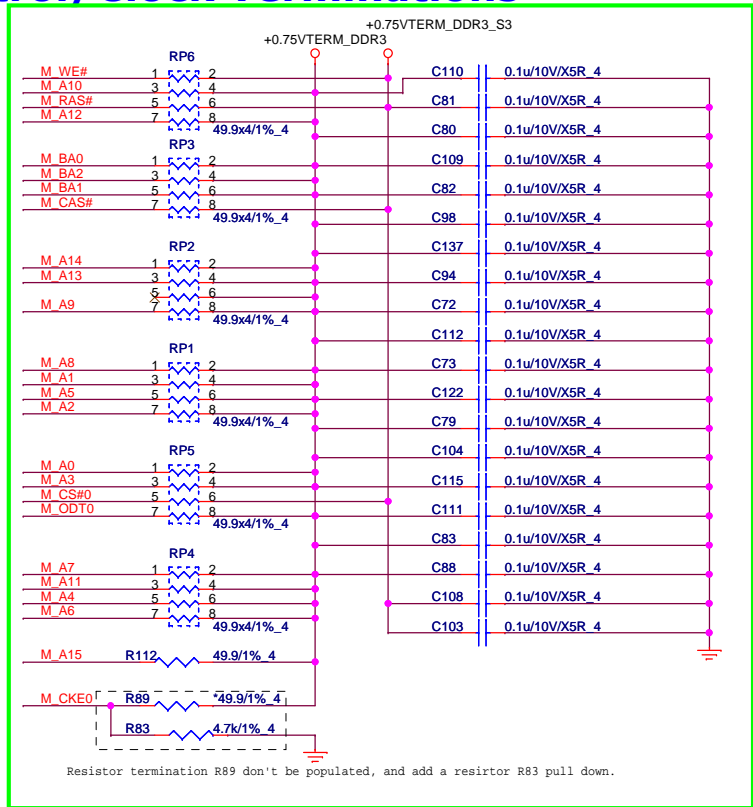


Quanta Computer Inc.
PROJECT : CL2

Size	Document Number	Rev
	DDR3 SDRAM	4A
Date: Monday, December 12, 2011		Sheet 10 of 35

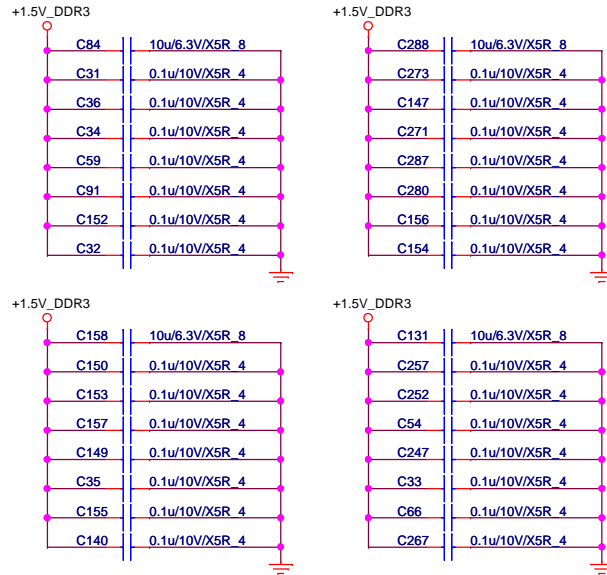
Address/Control/Clock Terminations

- (7,10) M_A[0:15] \rightarrow M_A[0:15]
- (7,10) M_BA[0:2] \rightarrow M_BA[0:2]
- (7,10) M_WE# \rightarrow M_WE#
- (7,10) M_RAS# \rightarrow M_RAS#
- (7,10) M_CAS# \rightarrow M_CAS#
- (7,10) M_CS#0 \rightarrow M_CS#0
- (7,10) M_ODT0 \rightarrow M_ODT0
- (7,10) M_CKE0 \rightarrow M_CKE0



1122 for consuming an additional 100mW.

DDR3 Power Decoupling

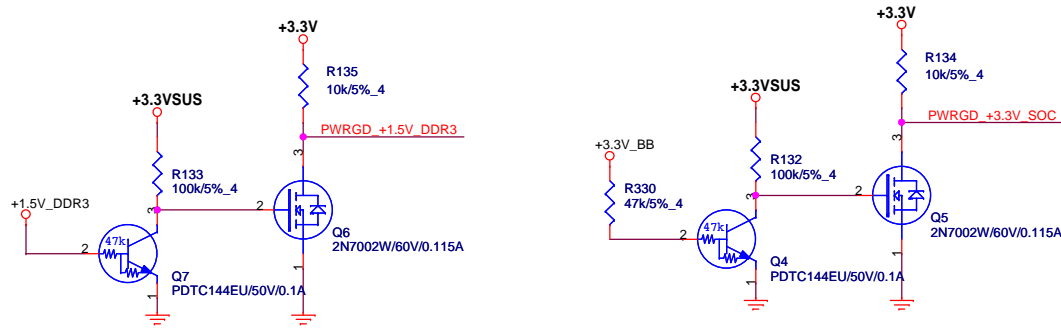


8 capacitors for each DRAM chip

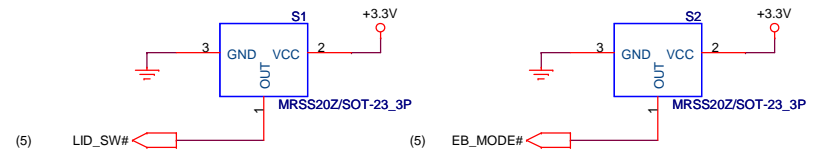
Quanta Computer Inc.
PROJECT : CL2

Size	Document Number	Rev
	DDR3 TERMINATION	4A
Date:	Monday, December 12, 2011	Sheet 11 of 35

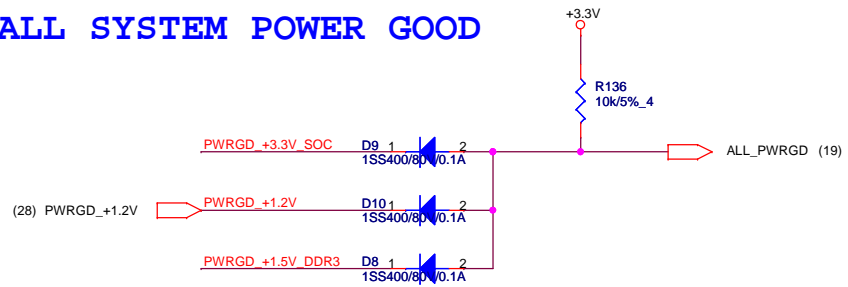
POWER GOOD



MR Sensor



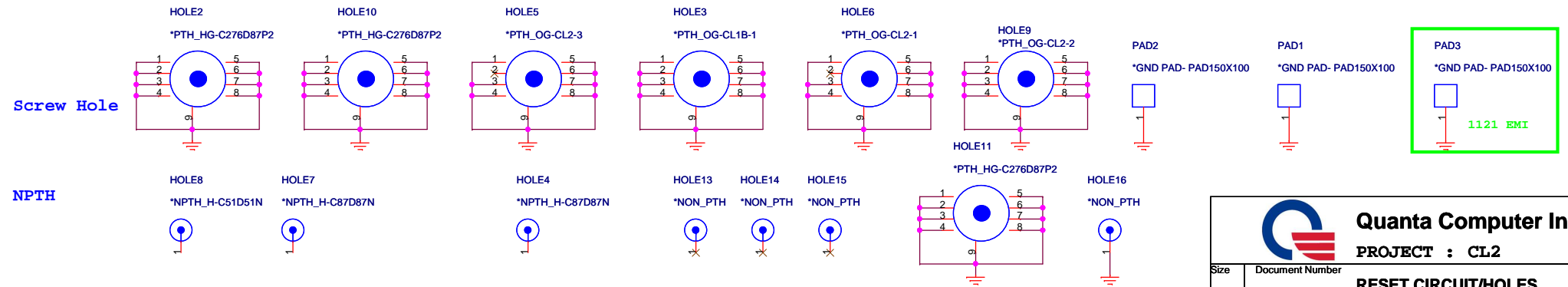
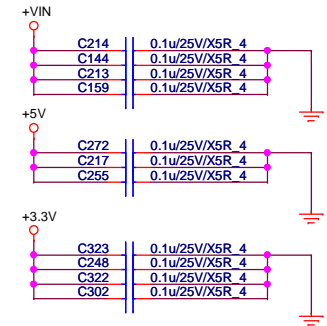
ALL SYSTEM POWER GOOD



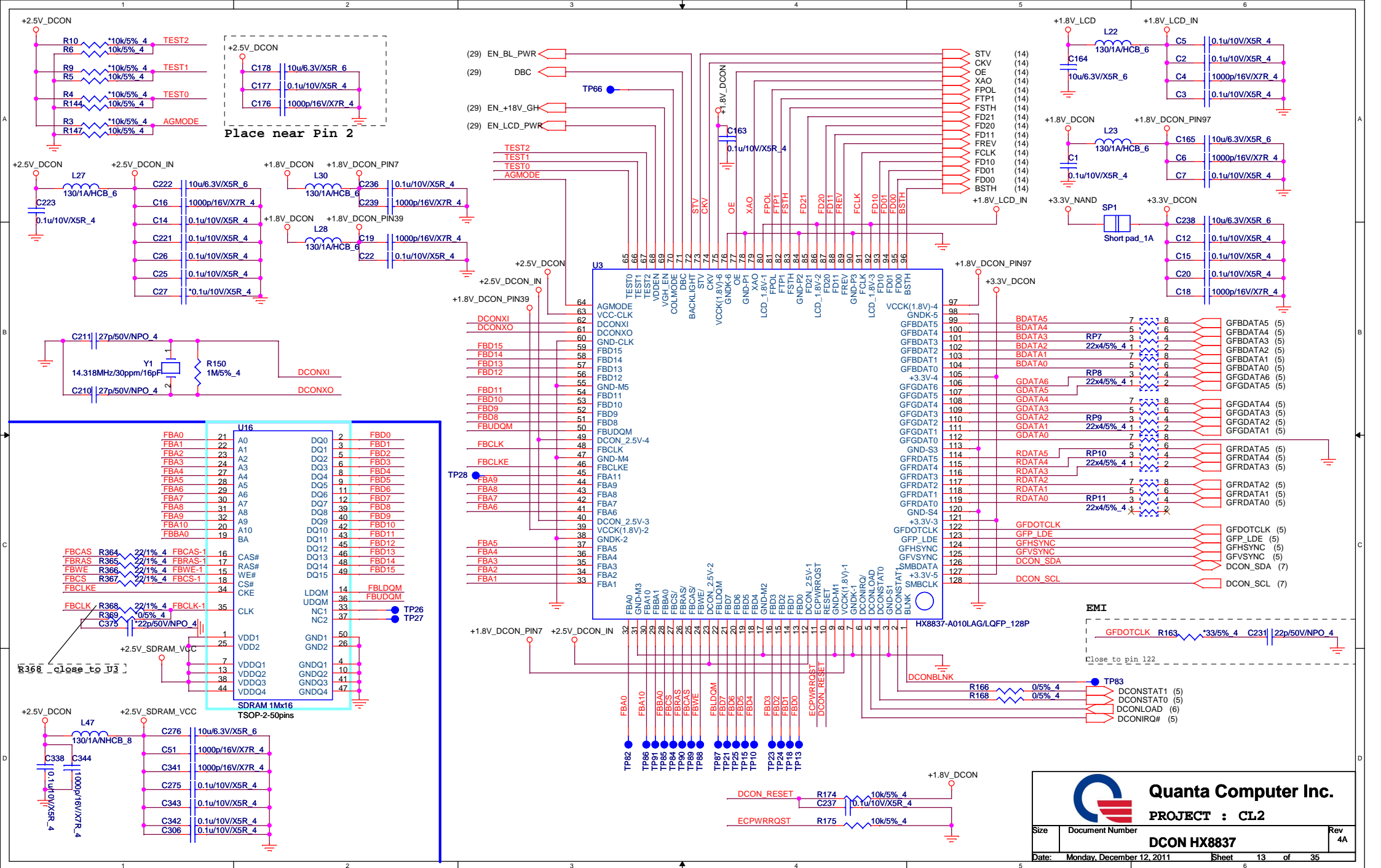
Depend on EC?

Modify by 07/27

EMI request



Quanta Computer Inc. PROJECT : CL2		Rev 4A
RESET CIRCUIT/HOLES		
Date:	Monday, December 12, 2011	Sheet 12 of 35



Quanta Computer Inc.
PROJECT : CL2

Size	Document Number	Rev
	DCON HX8837	4A
Date:	Monday, December 12, 2011	
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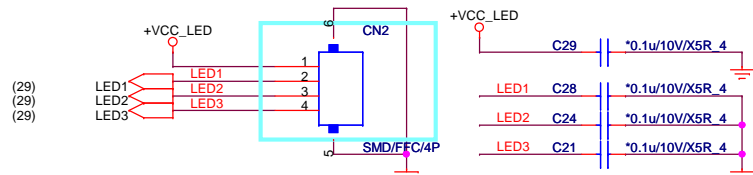
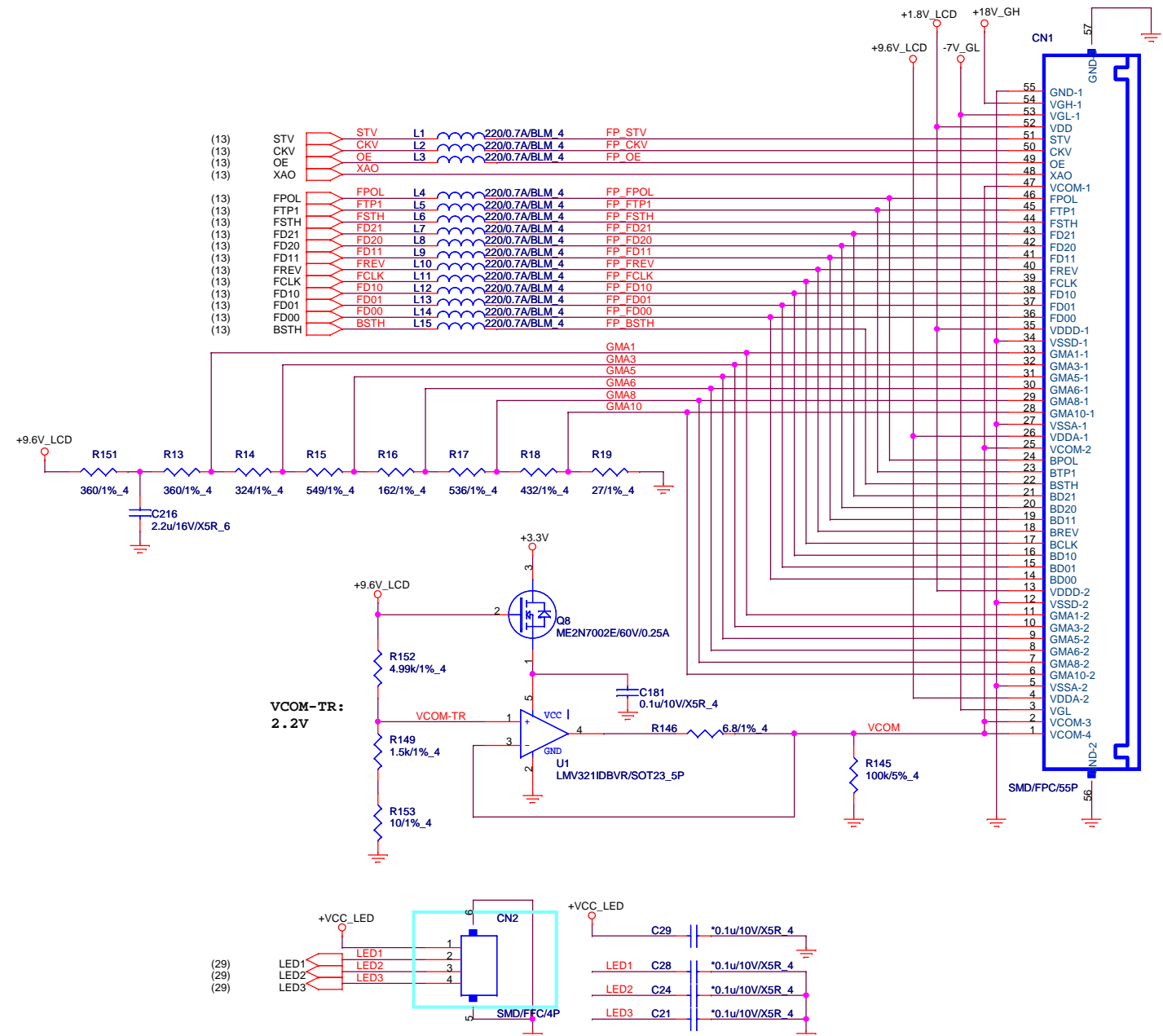
- (13) STV
- (13) CKV
- (13) OE
- (13) XAO
- (13) FPOL
- (13) FTP1
- (13) FSTH
- (13) FD21
- (13) FD20
- (13) FD11
- (13) FREV
- (13) FCLK
- (13) FD10
- (13) FD01
- (13) FD00
- (13) BSTH

- L1 220/0.7A/BLM 4
- L2 220/0.7A/BLM 4
- L3 220/0.7A/BLM 4
- L4 220/0.7A/BLM 4
- L5 220/0.7A/BLM 4
- L6 220/0.7A/BLM 4
- L7 220/0.7A/BLM 4
- L8 220/0.7A/BLM 4
- L9 220/0.7A/BLM 4
- L10 220/0.7A/BLM 4
- L11 220/0.7A/BLM 4
- L12 220/0.7A/BLM 4
- L13 220/0.7A/BLM 4
- L14 220/0.7A/BLM 4
- L15 220/0.7A/BLM 4
- FP STV
- FP CKV
- FP OE
- FP FPOL
- FP FTP1
- FP FSTH
- FP FD21
- FP FD20
- FP FD11
- FP FREV
- FP FCLK
- FP FD10
- FP FD01
- FP FD00
- FP BSTH

- GMA1
- GMA3
- GMA5
- GMA6
- GMA8
- GMA10

- FP STV C207 33p/50V/NPO 4
- FP CKV C206 33p/50V/NPO 4
- FP OE C205 33p/50V/NPO 4
- FP FPOL C204 33p/50V/NPO 4
- FP FTP1 C203 33p/50V/NPO 4
- FP FSTH C202 33p/50V/NPO 4
- FP FD21 C201 33p/50V/NPO 4
- FP FD20 C200 33p/50V/NPO 4
- FP FD11 C199 33p/50V/NPO 4
- FP FREV C198 33p/50V/NPO 4
- FP FCLK C197 33p/50V/NPO 4
- FP FD10 C196 33p/50V/NPO 4
- FP FD01 C195 33p/50V/NPO 4
- FP FD00 C194 33p/50V/NPO 4
- FP BSTH C193 33p/50V/NPO 4

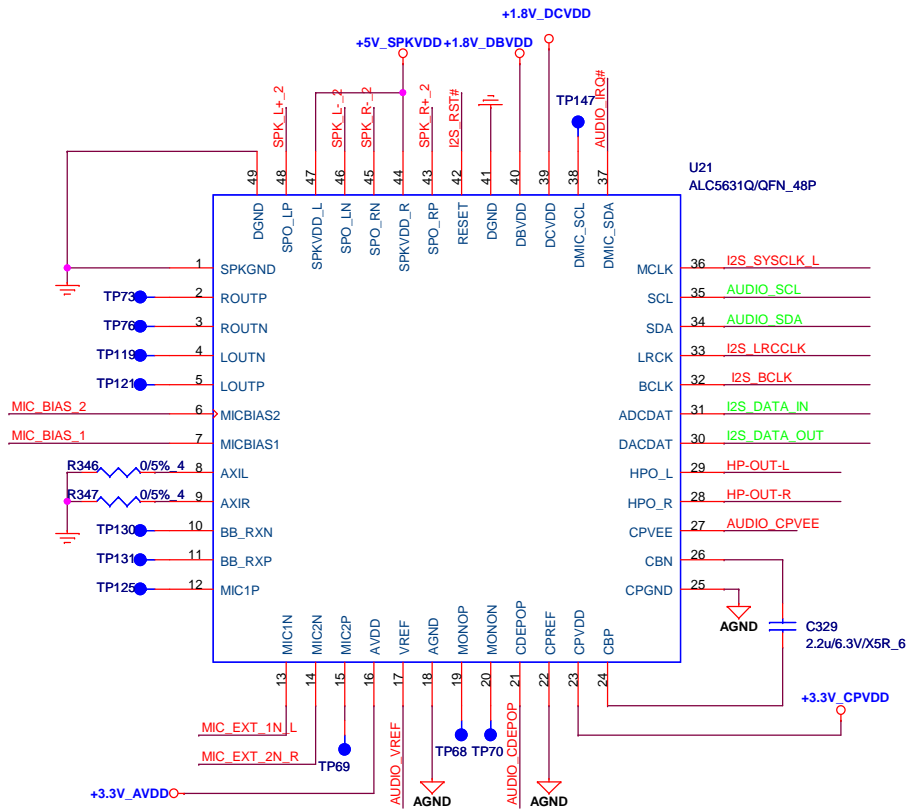
- FP FCLK L24 33nH/0.2A/HK 4
- C180 18p/50V/NPO 4
- GMA1 C192 0.1u/16V/X5R 4
- GMA3 C191 0.1u/16V/X5R 4
- GMA5 C190 0.1u/16V/X5R 4
- GMA6 C189 0.1u/16V/X5R 4
- GMA8 C188 0.1u/16V/X5R 4
- GMA10 C187 0.1u/16V/X5R 4
- C183 0.1u/10V/X5R 4
- C162 0.1u/10V/X5R 4
- C174 0.1u/10V/X5R 4
- C168 10u/6.3V/X5R 6
- C172 10u/6.3V/X5R 6
- C166 10u/6.3V/X5R 6
- C169 0.1u/16V/X5R 4
- C182 0.1u/16V/X5R 4
- C179 10u/16V/X5R 8
- C175 0.1u/10V/X5R 4
- C173 0.1u/10V/X5R 4
- C161 1u/16V/X5R 8



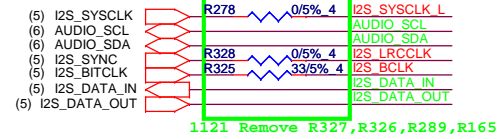
Quanta Computer Inc.
PROJECT : CL2

Size	Document Number	Rev
	LCD CONNECTOR	4A
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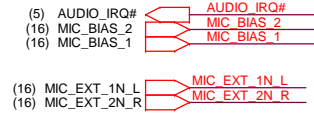
AUDIO CODEC



I2S:



INT/EXT MIC:

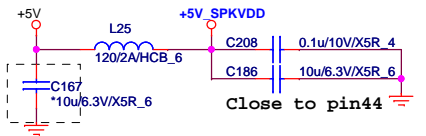
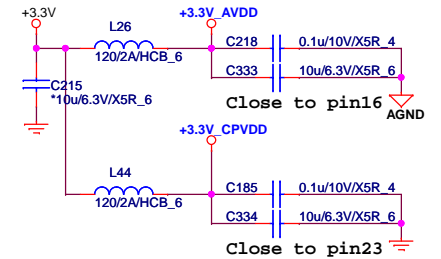
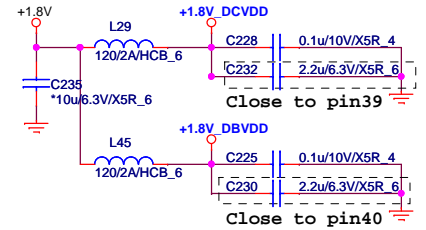


H/P OUT & SPK OUT:

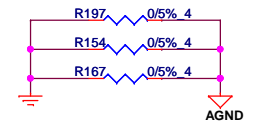


1201 Add LC filter on speakout.

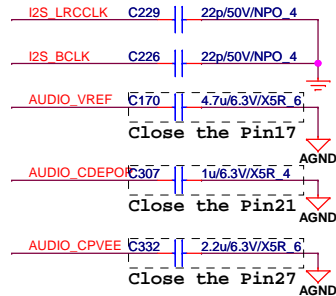
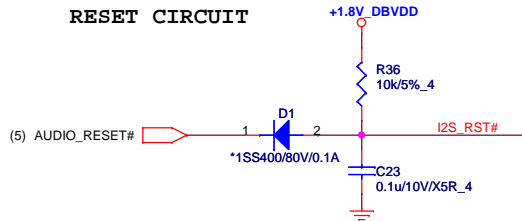
Audio Power




Analog Ground of Audio



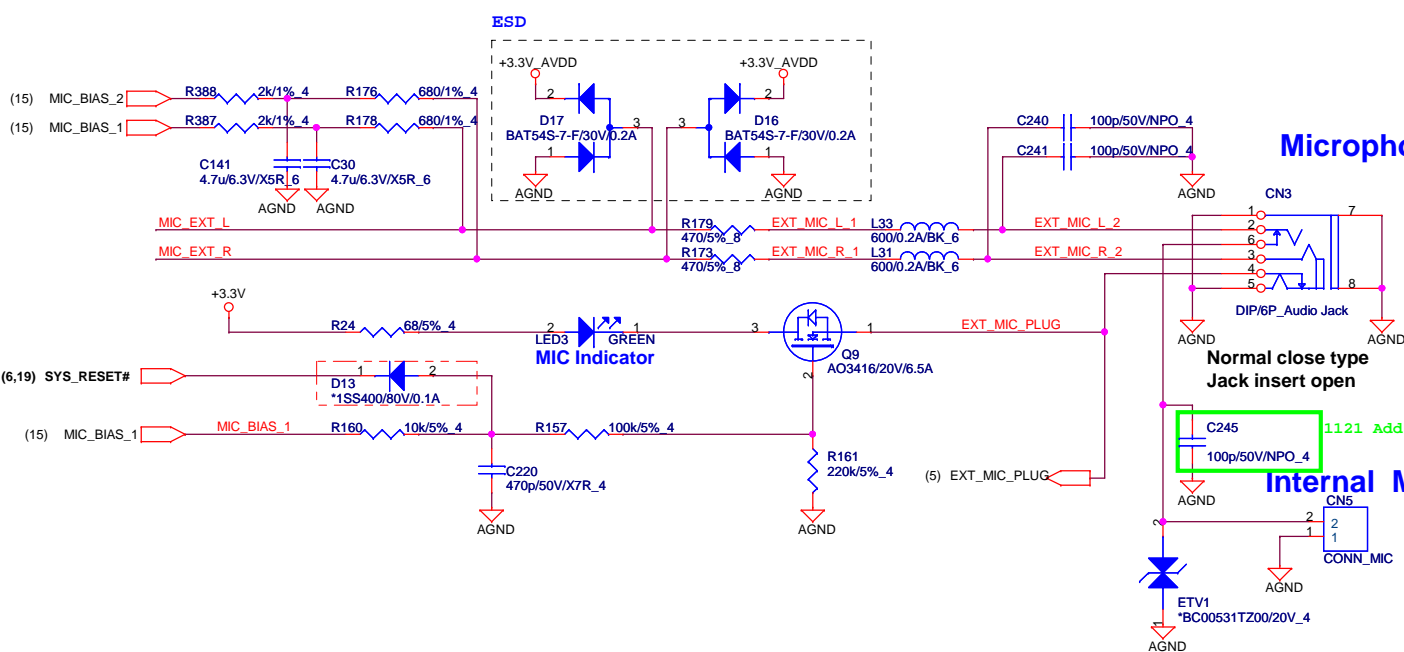
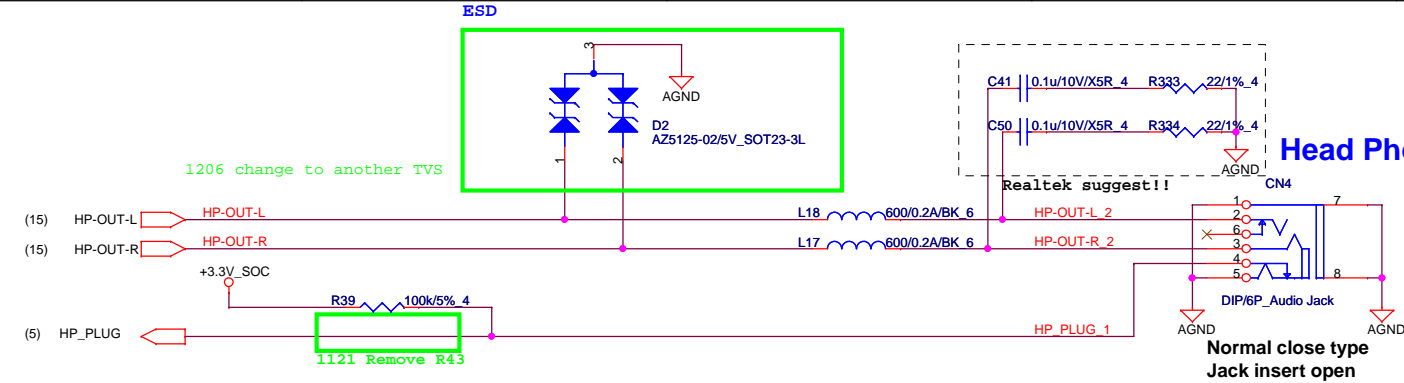
RESET CIRCUIT





Quanta Computer Inc.
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Size	Document Number	Rev
	I2S AUDIO CODEC ALC5631Q	4A
Date:	Monday, December 12, 2011	Sheet 15 of 35

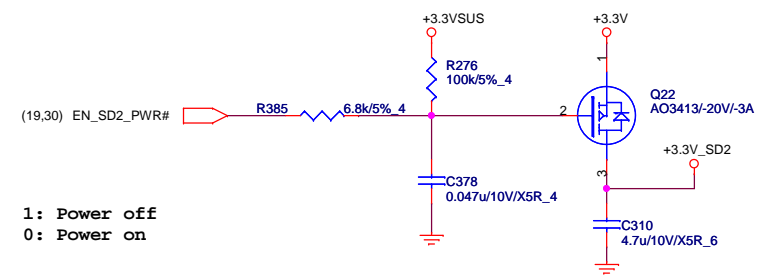
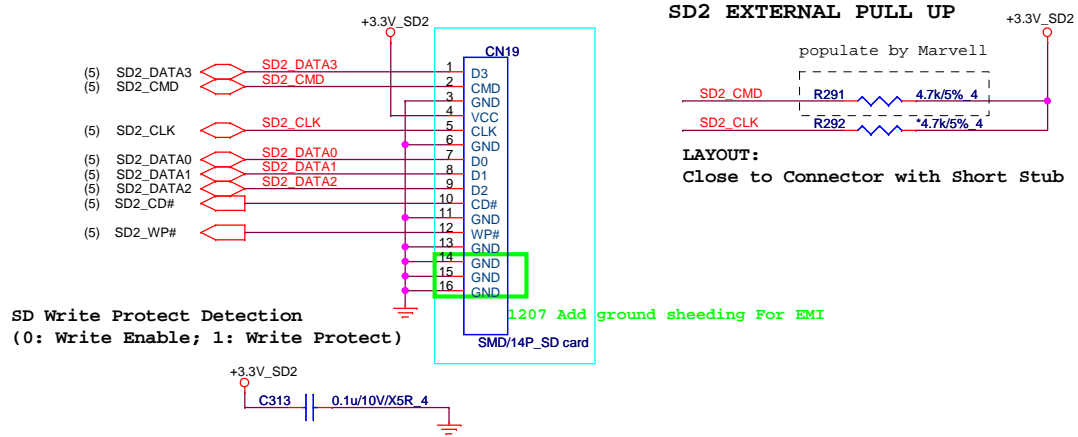


HIGH: DC Coupling
LOW: AC Coupling

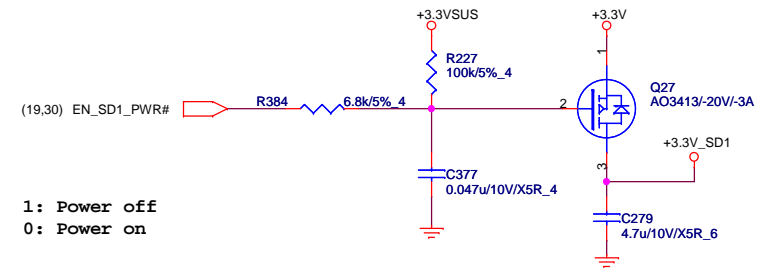
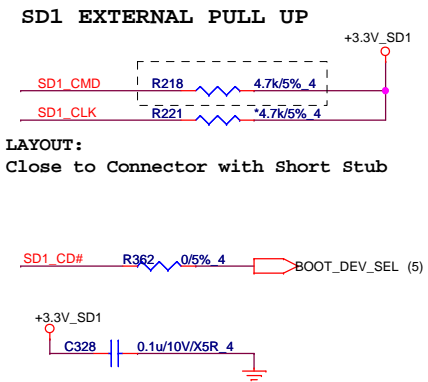
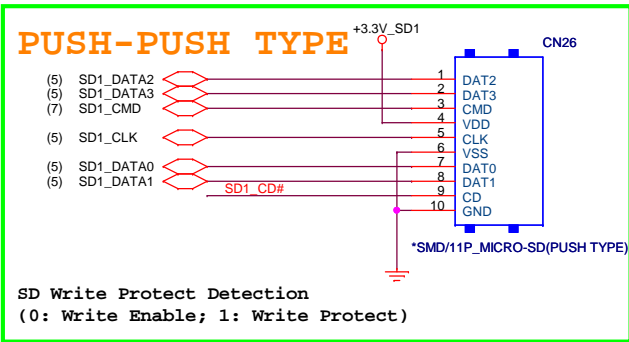
Quanta Computer Inc.
PROJECT : CL2

Size	Document Number	Rev
	AUDIO JACKS	4A
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Ext. SD2 Card Reader

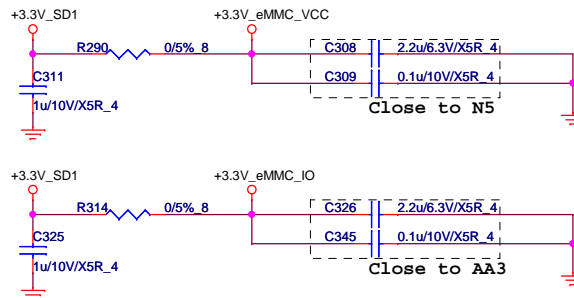
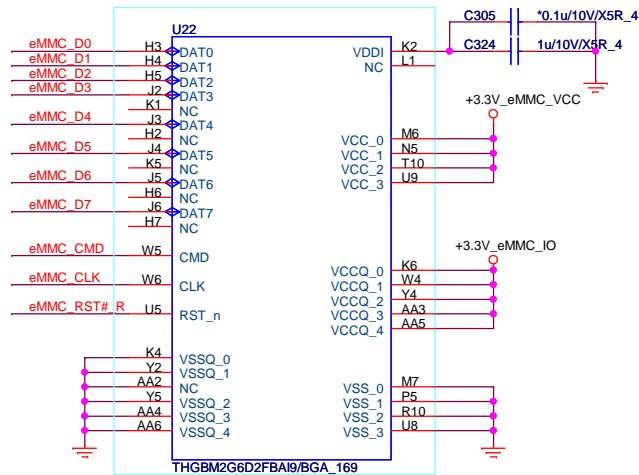


Int. SD1 Card Reader

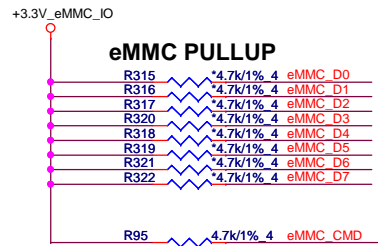


Quanta Computer Inc.
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Size	Document Number	Rev
	MICRO SD & SD SLOTS	4A
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- (7) eMMC_D0 TP183
- (7) eMMC_D1 TP182
- (7) eMMC_D2 TP181
- (7) eMMC_D3 TP180
- (7) eMMC_D4 TP159
- (7) eMMC_D5 TP158
- (7) eMMC_D6 TP156
- (7) eMMC_D7 TP140
- (7) eMMC_CMD TP146
- (7) eMMC_CLK TP145
- (7) eMMC_RST# eMMC_RST#
1121 Remove R313



TOS -- 8G -- AKE3QZPT000
 TOS -- 16G -- AKE3RZPT000
 TOS -- 32G -- AKE3SZPT001

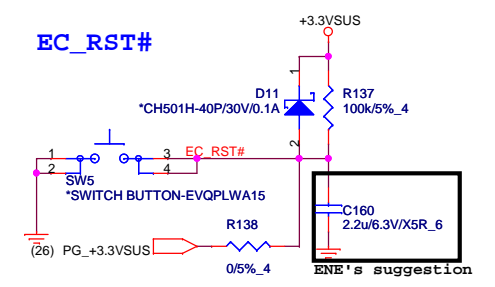
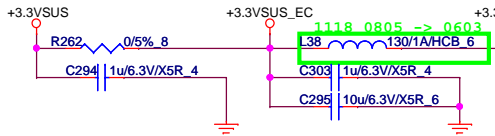
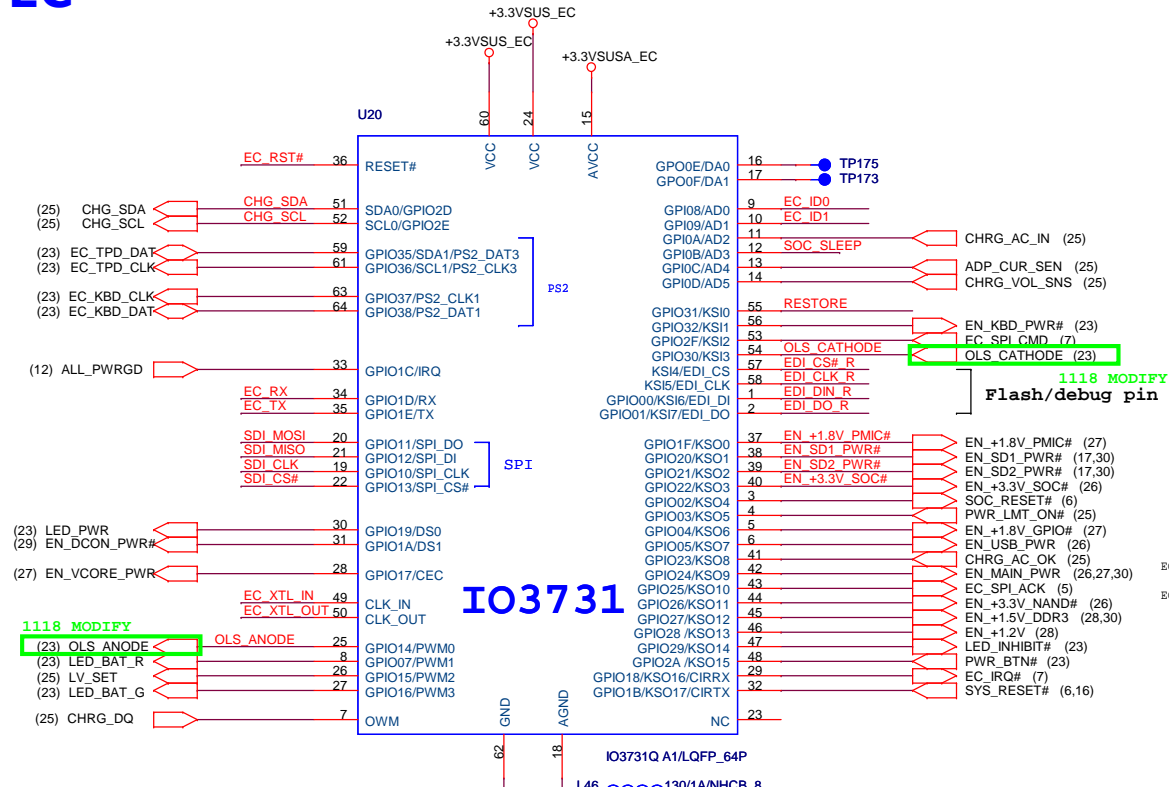
HYU -- 8G -- AKE3QZUTW00 ???
 HYU -- 16G -- AKE3RZUTW00
 HYU -- 32G -- AKE3SZUTW00

SAM -- 8G -- AKE3QZPT503
 SAM -- 16G -- AKE3RZPT501
 SAM -- 32G -- AKE3SZPT500



Quanta Computer Inc.
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Size	Document Number	Rev
	NAND FLASH (MLC)	4A
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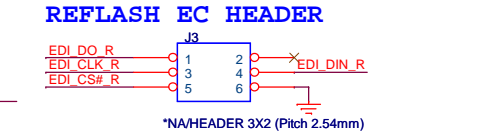
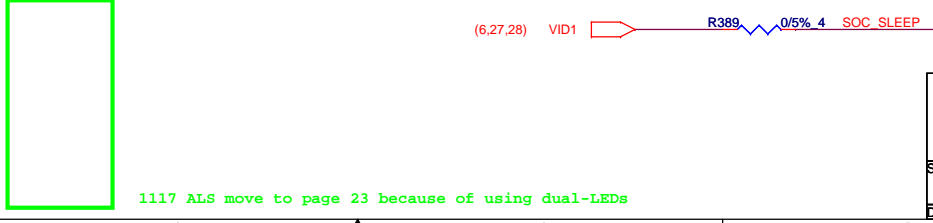
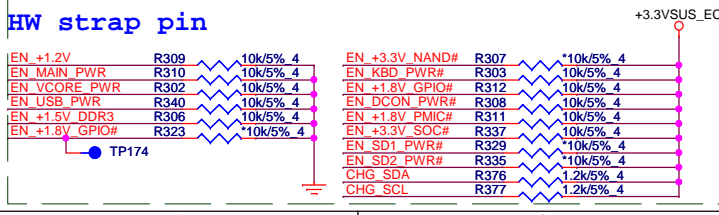
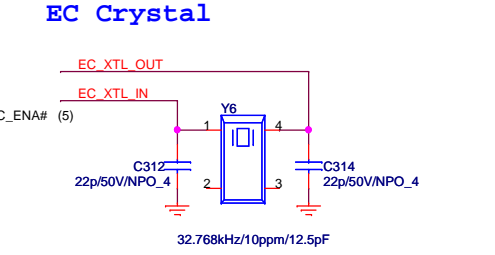
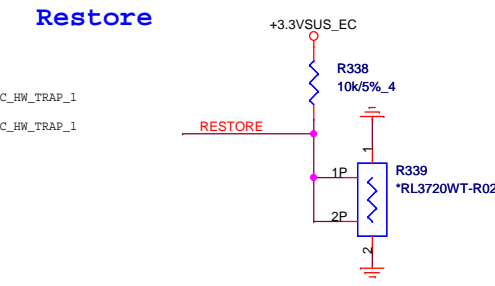
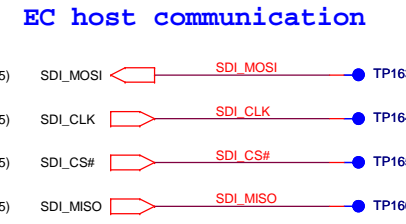
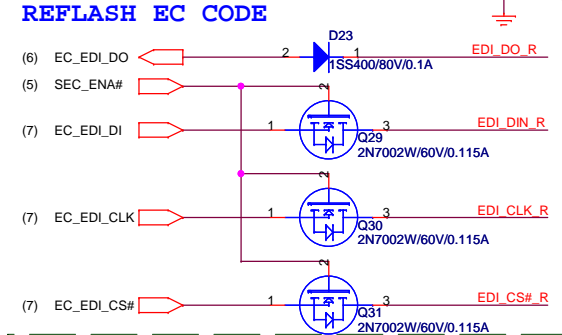
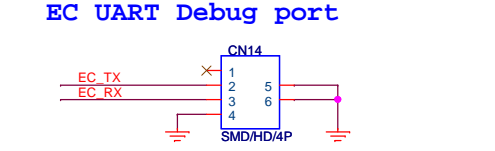
EC_ID change to these value

EC ID0	Setting
A1	1/8*3VSUS
A2	2/8*3VSUS
A3	3/8*3VSUS
B1	4/8*3VSUS
C1	5/8*3VSUS
D	2/8*3VSUS

EC ID0 is connected to R268 (30k/1% 4) and R269 (10k/1% 4) resistors.

EC ID1	Setting
A1-B	1/8*3VSUS
C1-D	2/8*3VSUS

EC ID1 is connected to R267 (30k/1% 4) and R266 (10k/1% 4) resistors.



1117 ALS move to page 23 because of using dual-LEDs

Quanta Computer Inc.

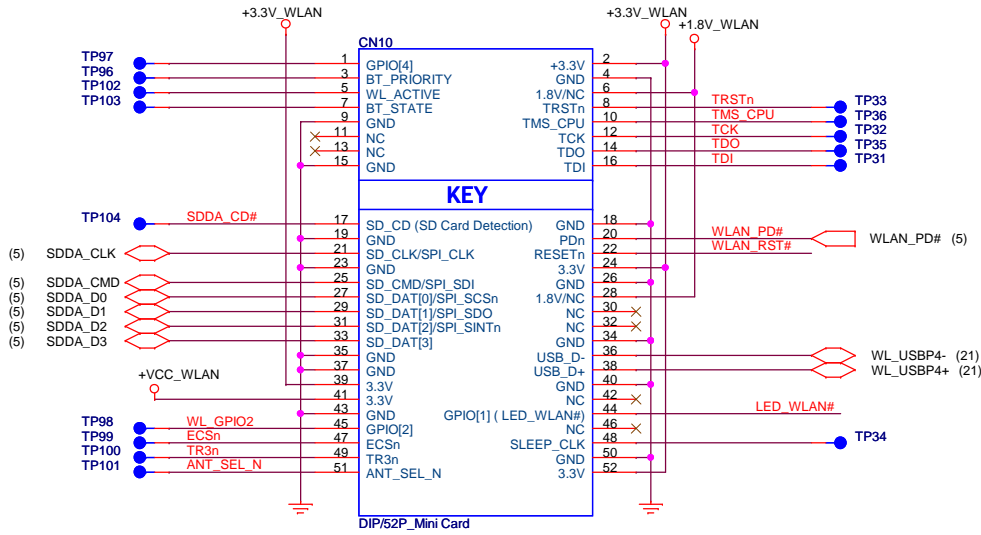
PROJECT : CL2

EC IO3731

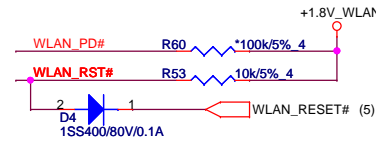
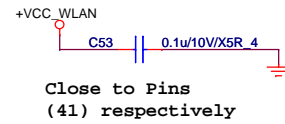
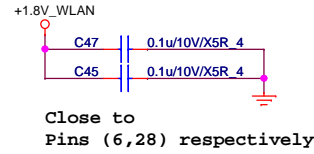
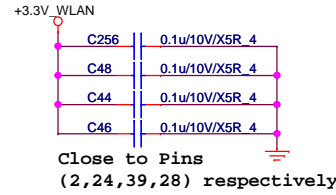
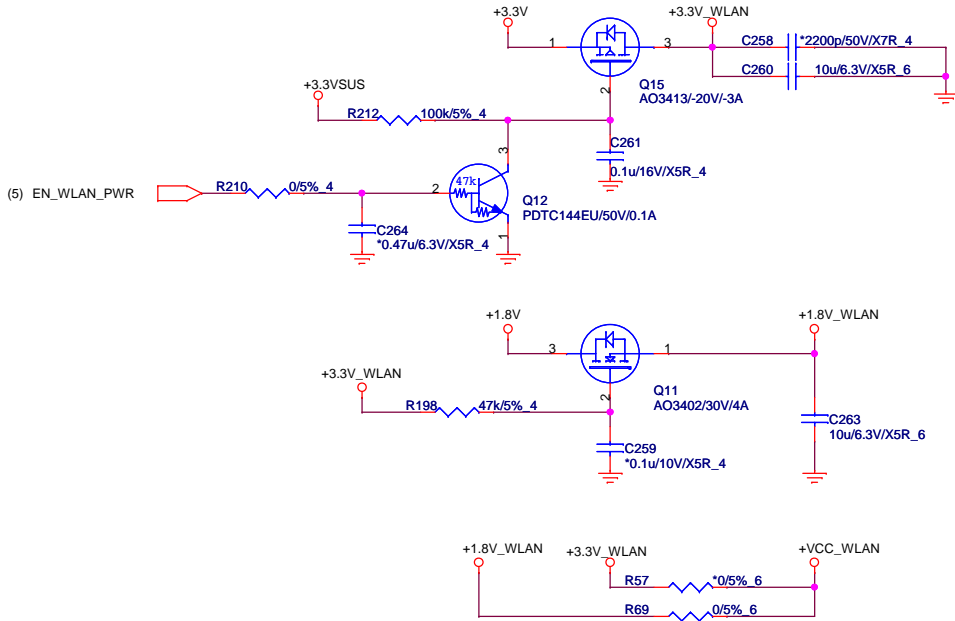
Size	Document Number	Rev
		4A

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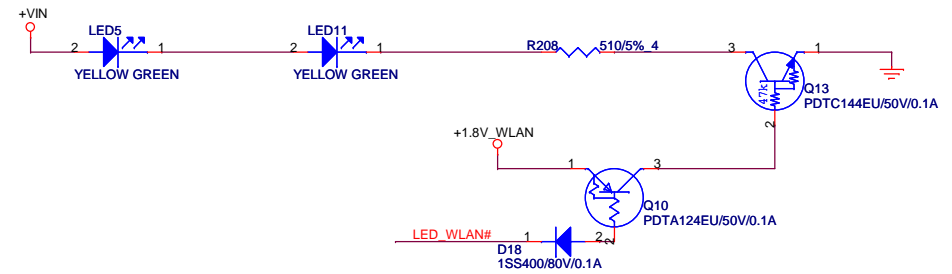
WLAN Module in Mini-PCIE Socket
Only for SDIO & USB Devices



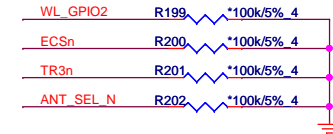
WLAN Power



WLAN LED Indicator



GPIO[4]:
WLAN MAC wake-up input/interrupt input



WL_GPIO[2] boot-up configuration:
 0: JTAG mode enabled (pulled low by 100kohms)
 1 and floating: JTAG mode disabled (Default)

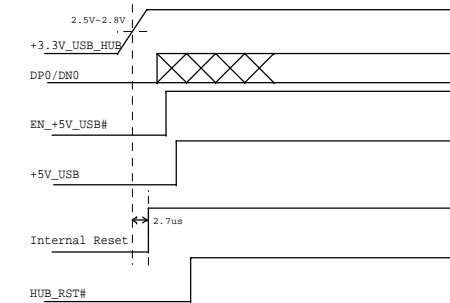
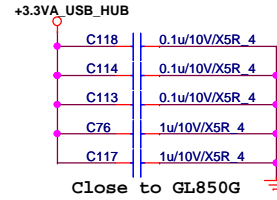
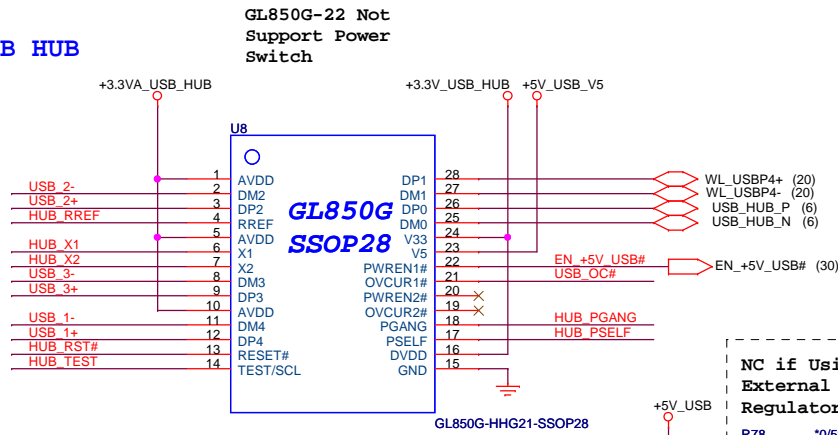
[ANT_SEL_N, TR3n] boot-up configuration:
 00: General SPI (pulled-low by 100kohms)
 11: SDIO (floating is OK)

ECSn boot-up configuration:
 0: Boot up from SPI EEPROM (pulled-low by 100kohms)
 1 & floating: Boot up from host interface (Default)

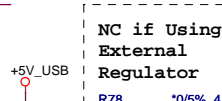


Size	Document Number	Rev
		4A
WLAN MODULE (SDIO)		
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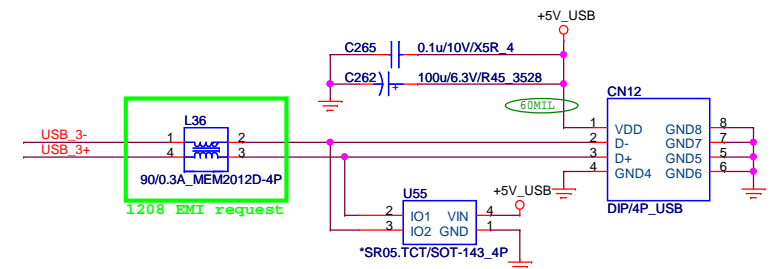
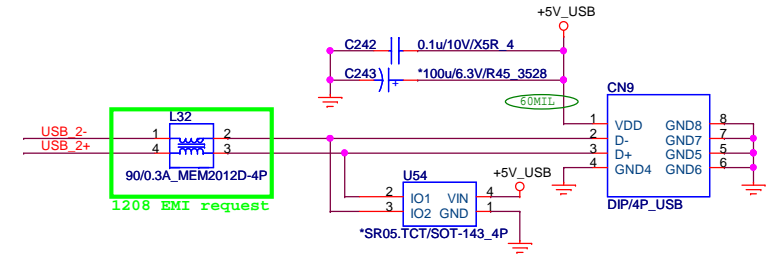
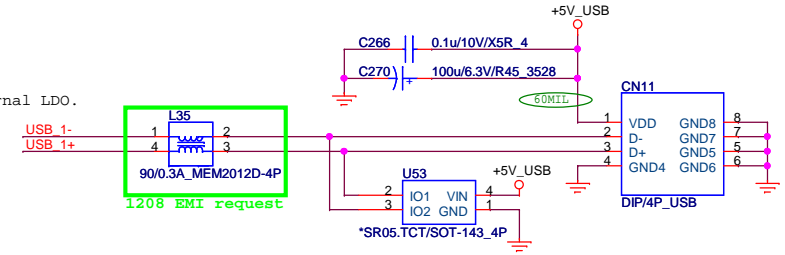
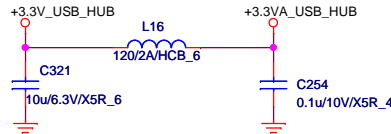
USB HUB



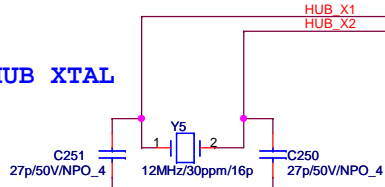
USB PORTS



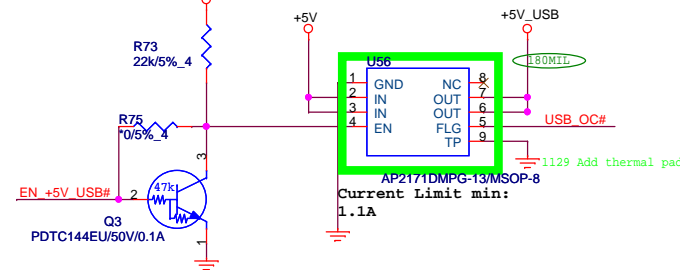
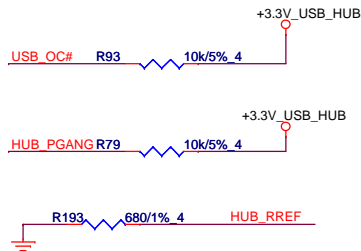
On HUB IC, V33 is generated by V5 if using internal LDO.



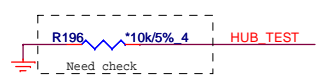
USB HUB XTAL



Close to GL850G



HUB_TEST
 0: Normal Operation (Internal PD)
 1: Test Mode



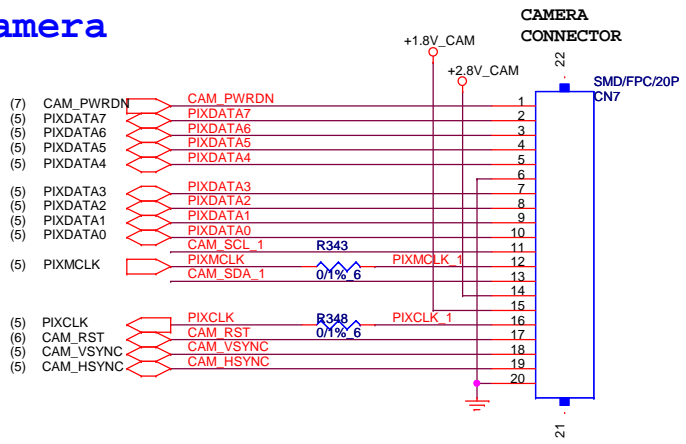
HUB_PSELF
 0: GL850G is bus-powered
 1: GL850G is self-powered



Quanta Computer Inc.
PROJECT : CL2

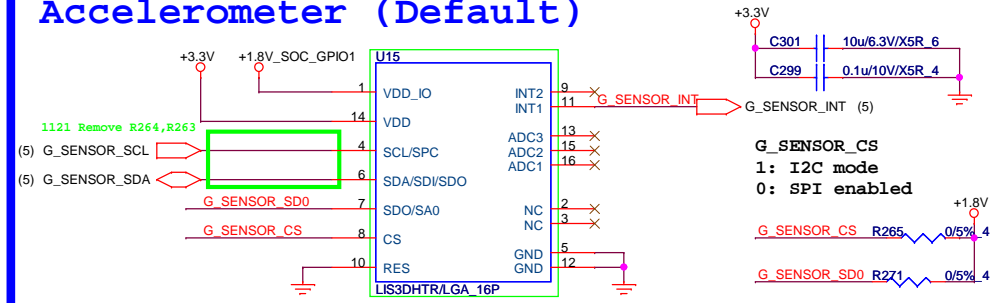
Size	Document Number	Rev
	USB HUB & PORTS	4A
Date:	Monday, December 12, 2011	Sheet 21 of 35

Camera

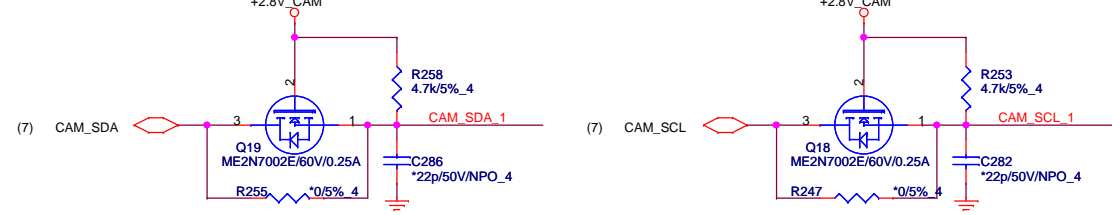
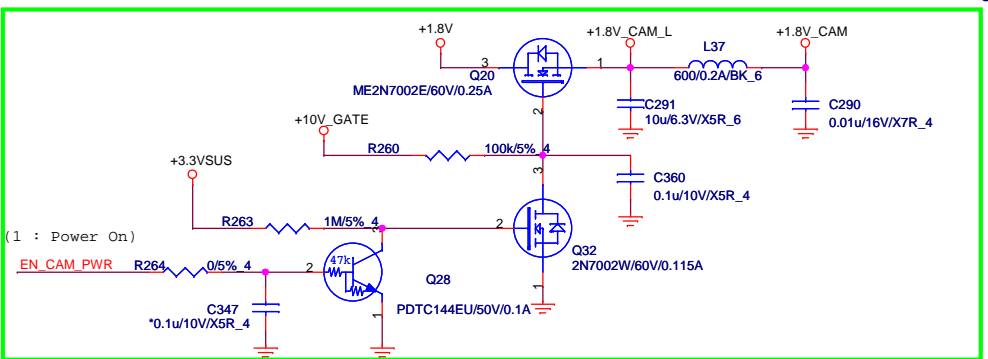
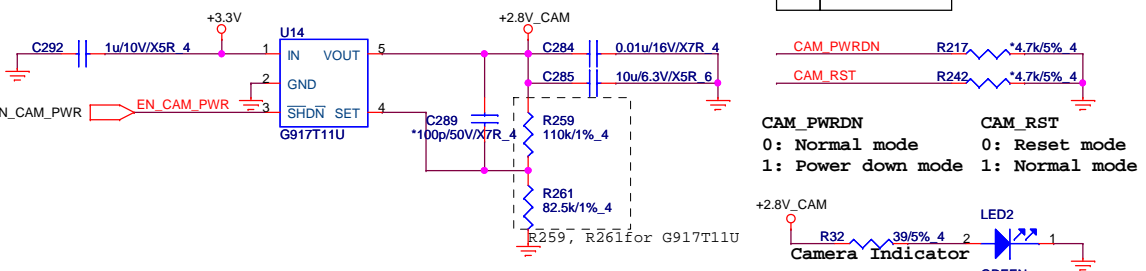
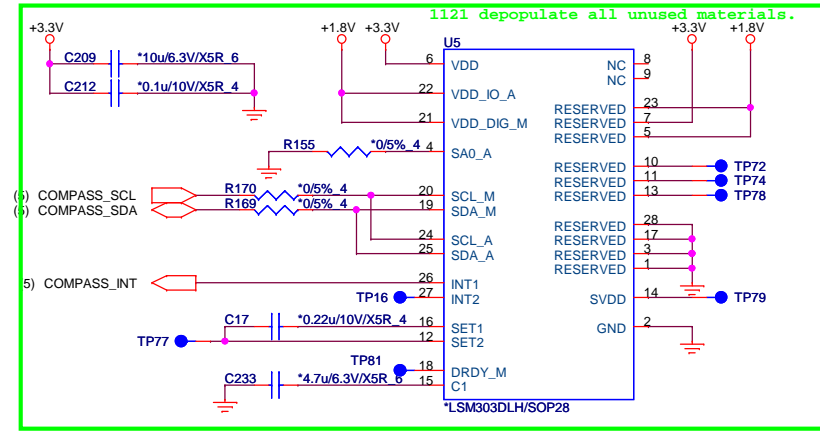


No.	Signal
1	PWDN
2	D7
3	D6
4	D5
5	D4
6	D3
7	D2
8	D1
9	D0
10	I2C Clock
11	MXCK (XCLK)
12	I2C Data
13	DOVDD
14	DVDD
15	PIXCLK
16	RESET
17	VSYNC
18	HSYNC
19	AGND
20	AGND

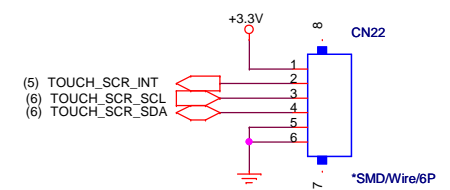
Accelerometer (Default)



Combo Chip (Optional) 3D Accelerometer & Digital Compass



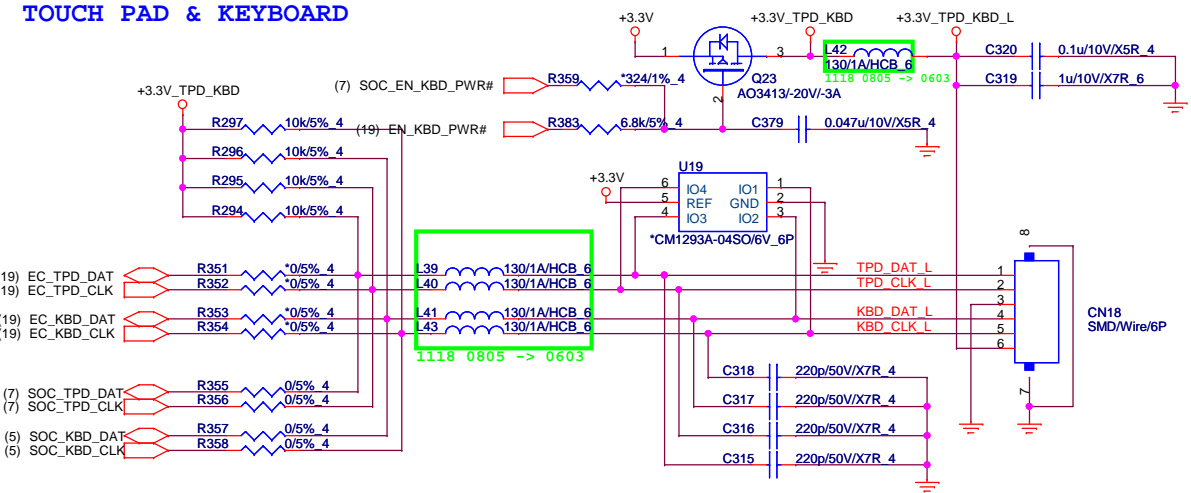
Touch Screen (Optional)



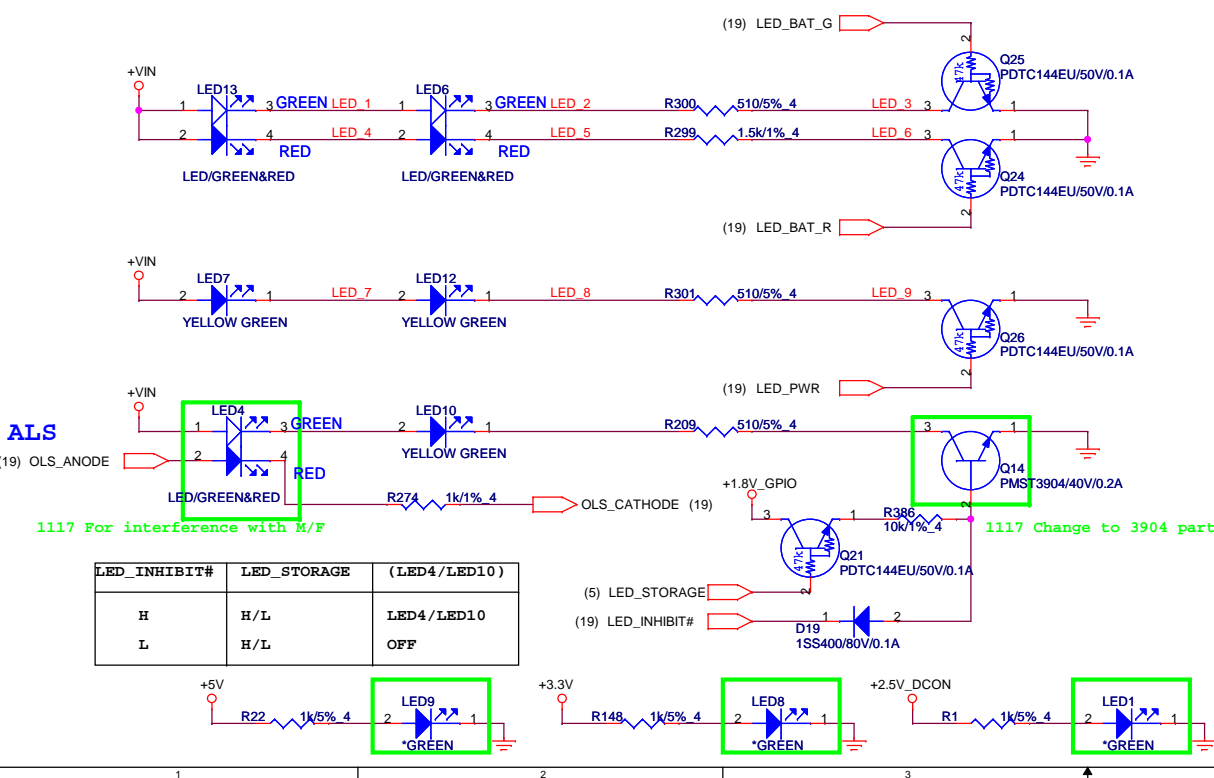
Quanta Computer Inc.
PROJECT : CL2

Size	Document Number	Rev
CAMERA & G-SENSOR & TOUCH		4A
Date:	Monday, December 12, 2011	Sheet 22 of 35

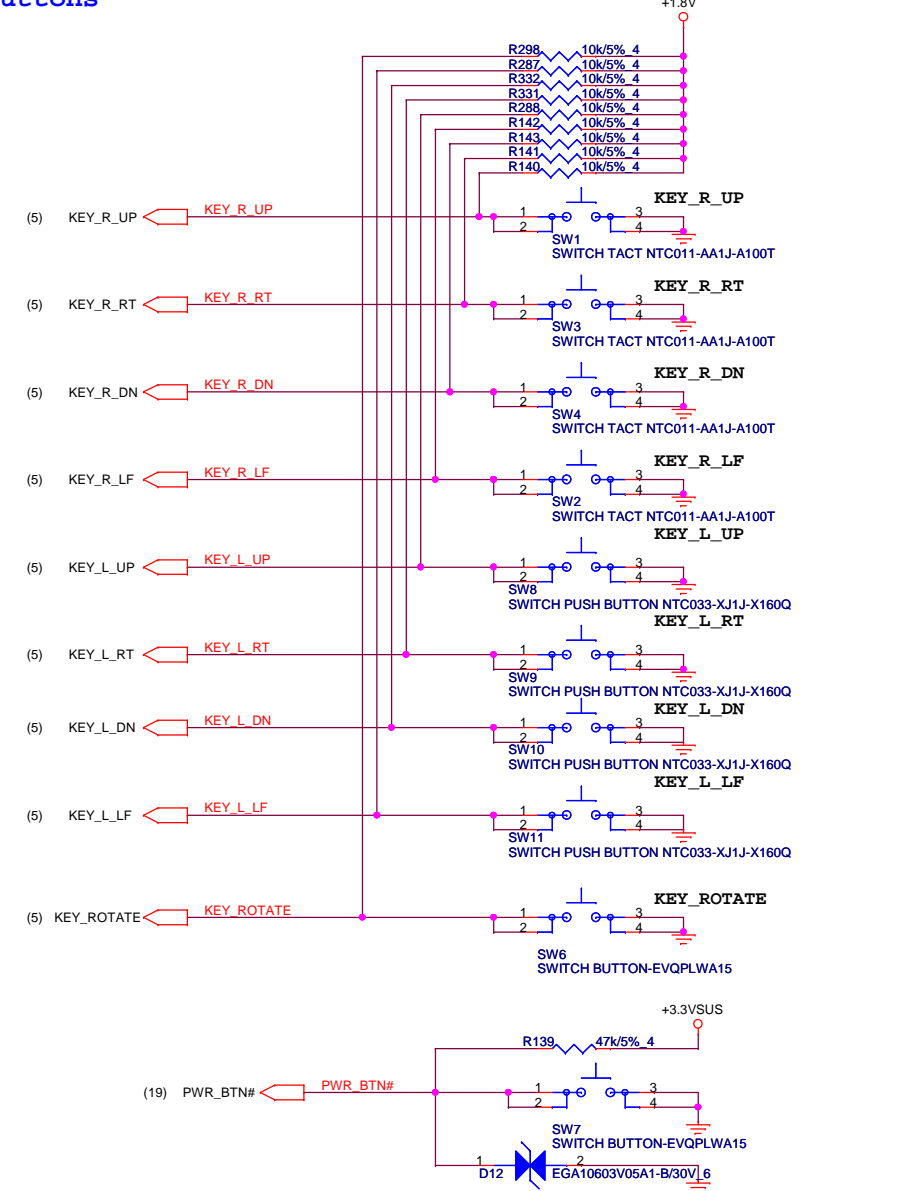
TOUCH PAD & KEYBOARD




LED (Battery, Power & Storage)



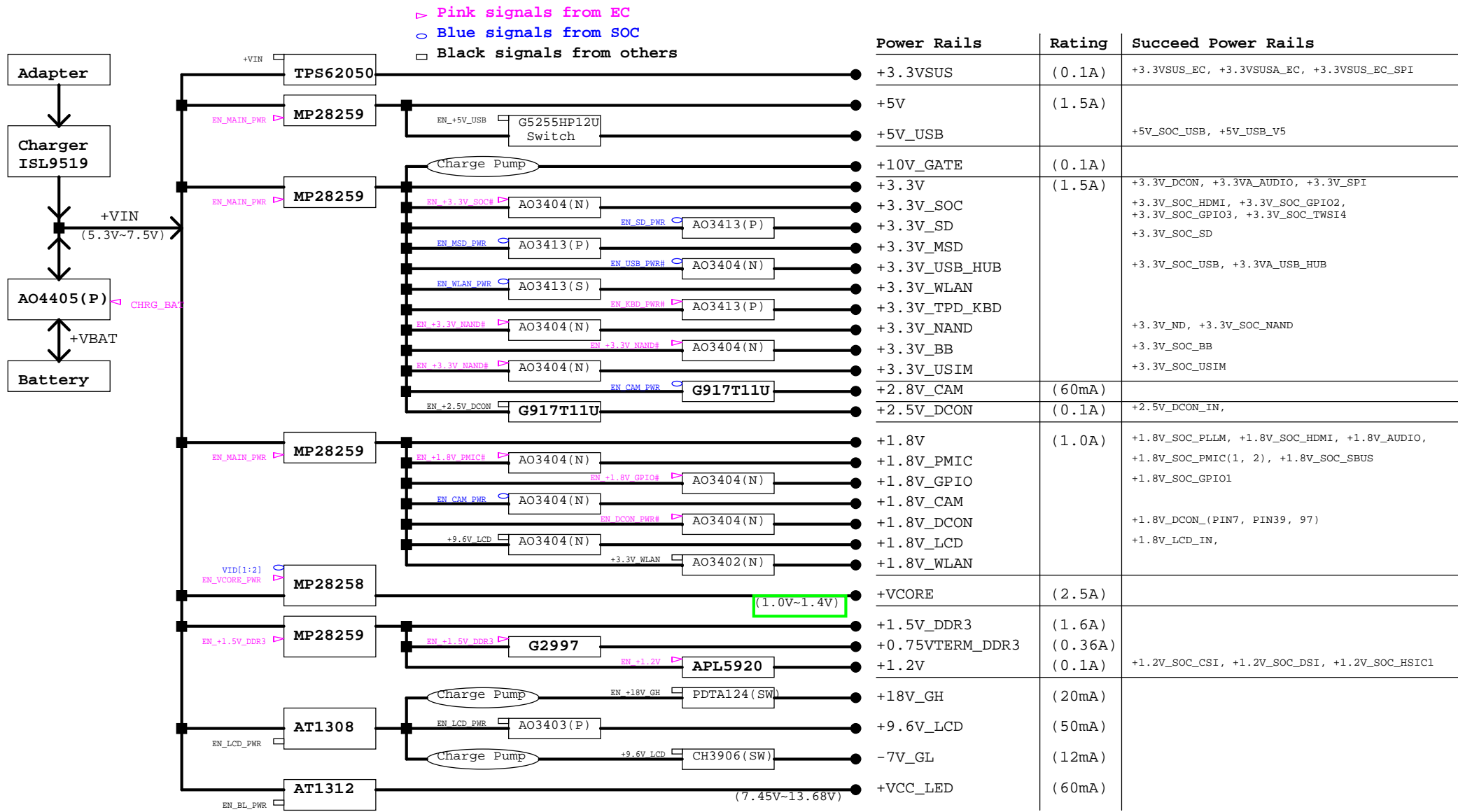
Buttons






Quanta Computer Inc.
PROJECT : CL2

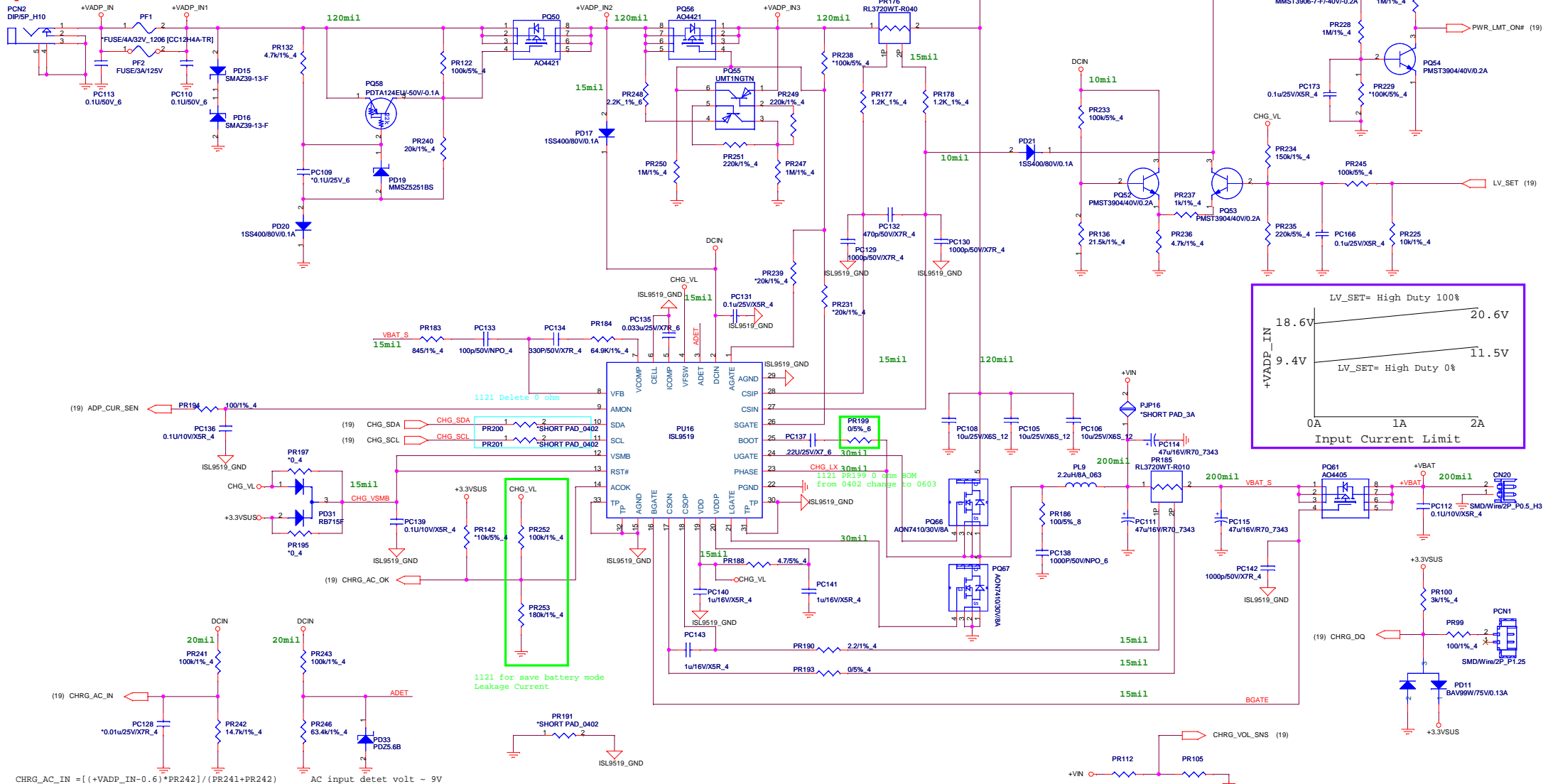
Size	Document Number	Rev
	TPD/KBD/LED/MR SENSOR/BUTTON	4A
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 PROJECT : CL2

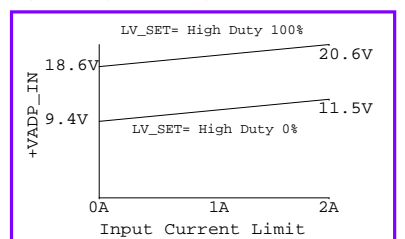
Size	Document Number	Rev
	POWER MAP	4A
Date:	Monday, December 12, 2011	Sheet 24 of 35

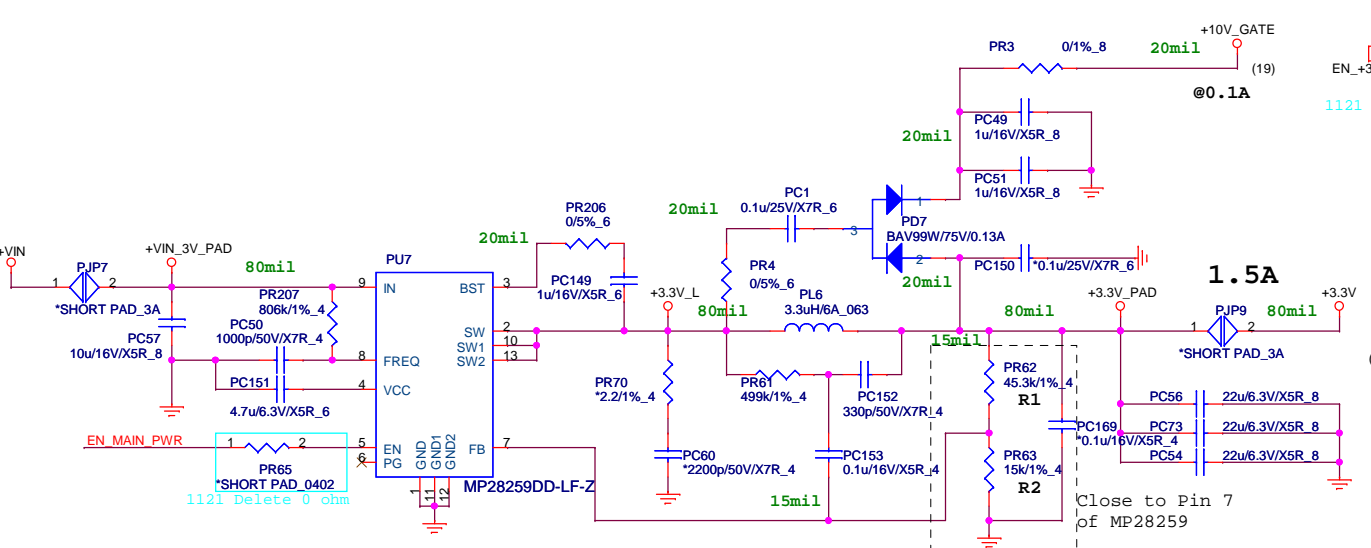
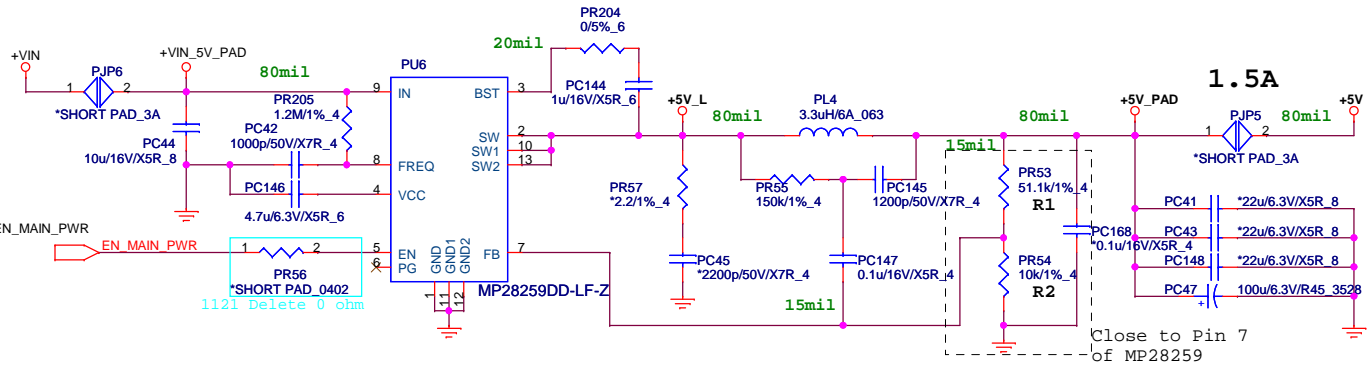
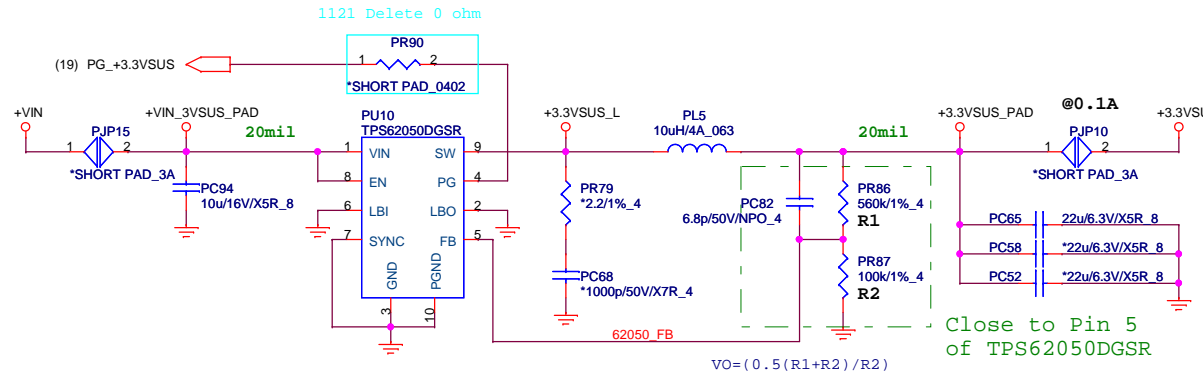
Input 10.5V-25V



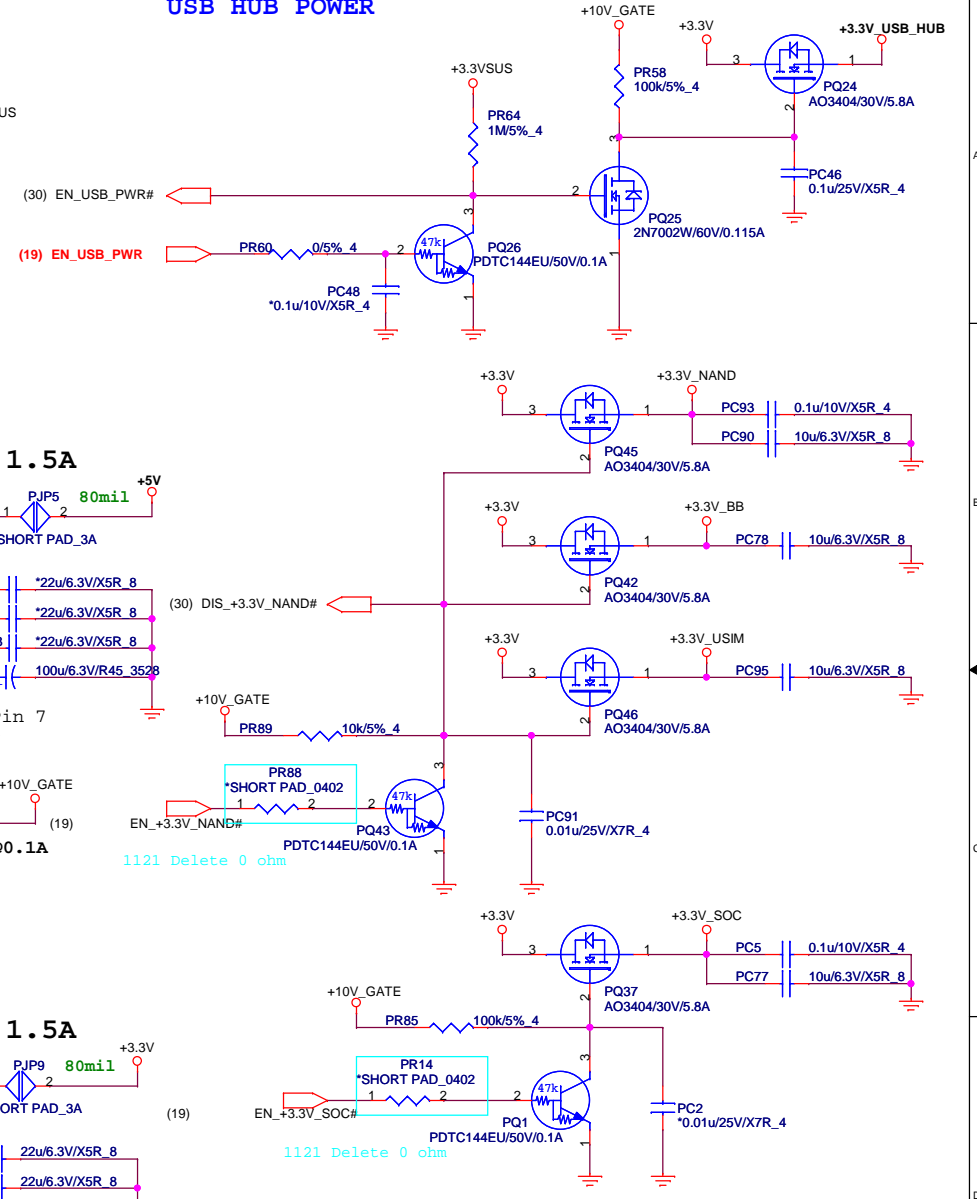
CHRG_AC_IN = [(+VADP_IN-0.6)*PR242]/(PR241+PR242)

AC input detet volt ~ 9V



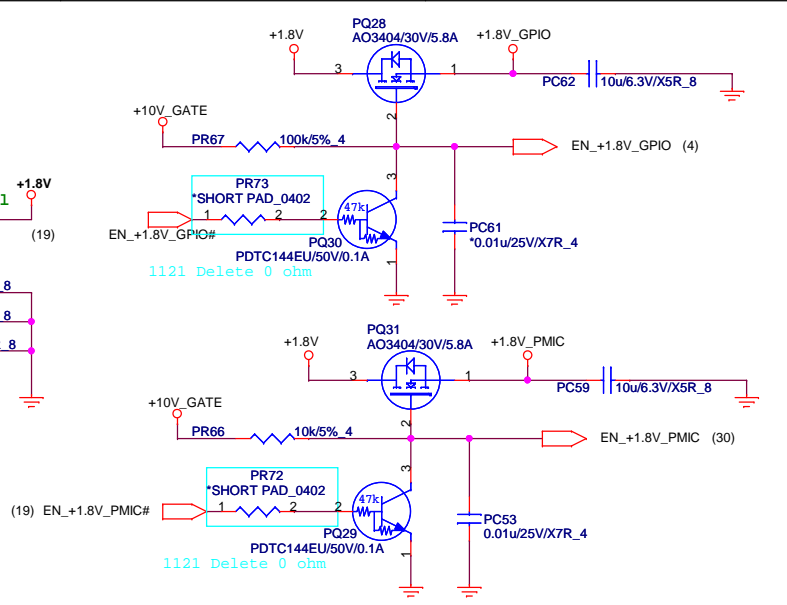
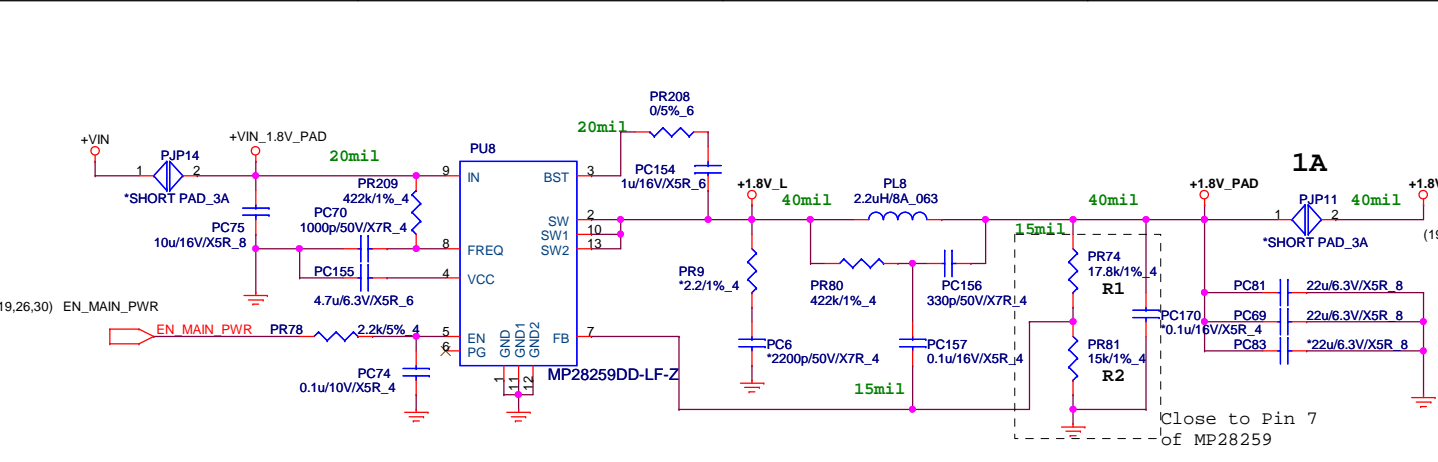


USB HUB POWER



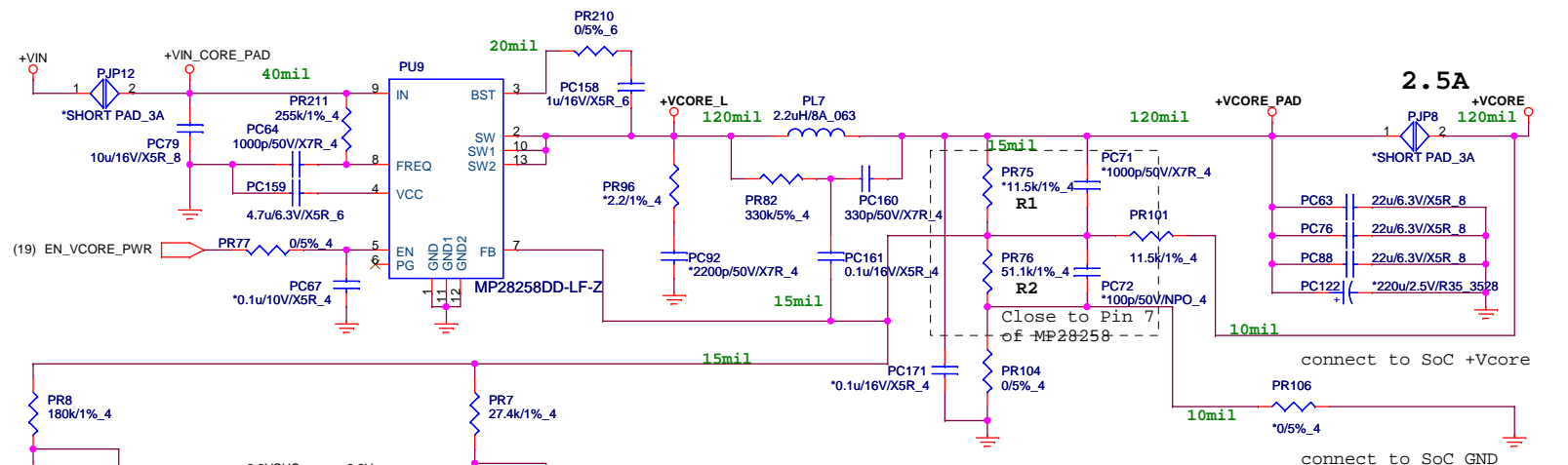
Quanta Computer Inc.
PROJECT : CL2

Size	Document Number	Rev
	PWR (2/6) +3.3VSUS/+5V/+3.3V	4A
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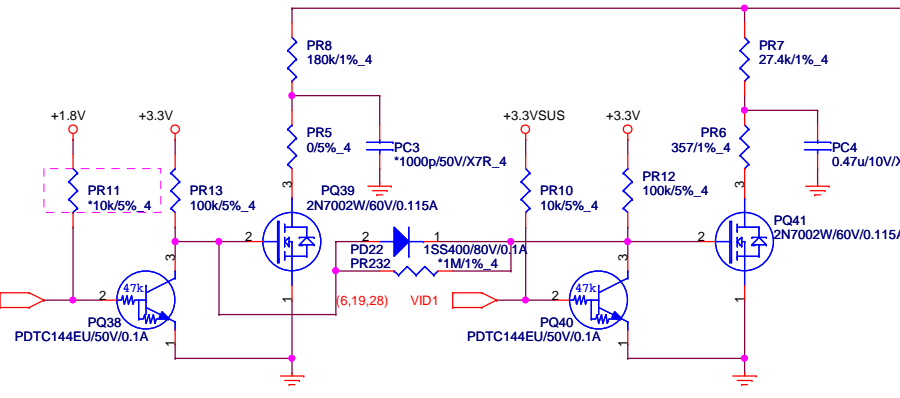
SOC CORE POWER (+VCORE)

VID1	VID2	+VCORE
0	0	1.4V
0	1	1.35V
1	X	1V
1	X	1V



Depend on GPIO PIN

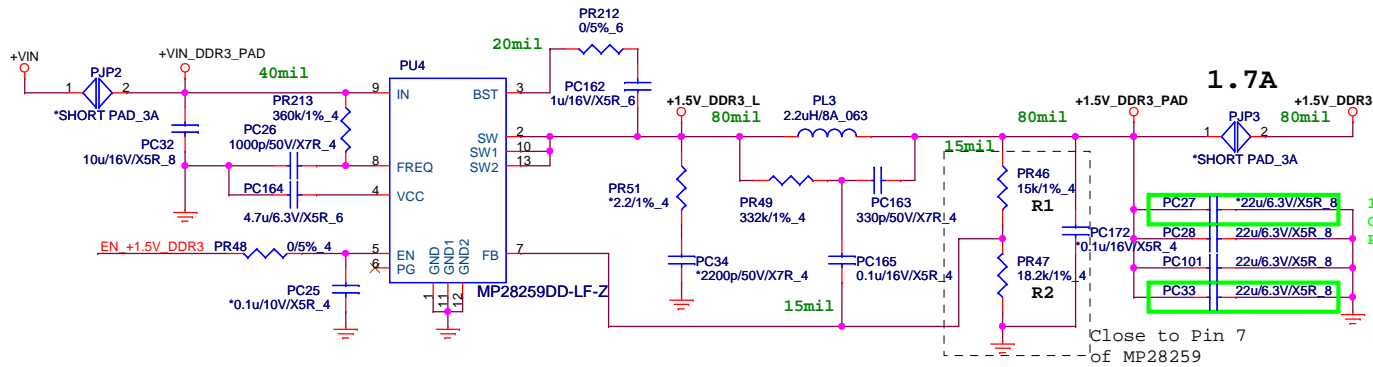
(5) VID2



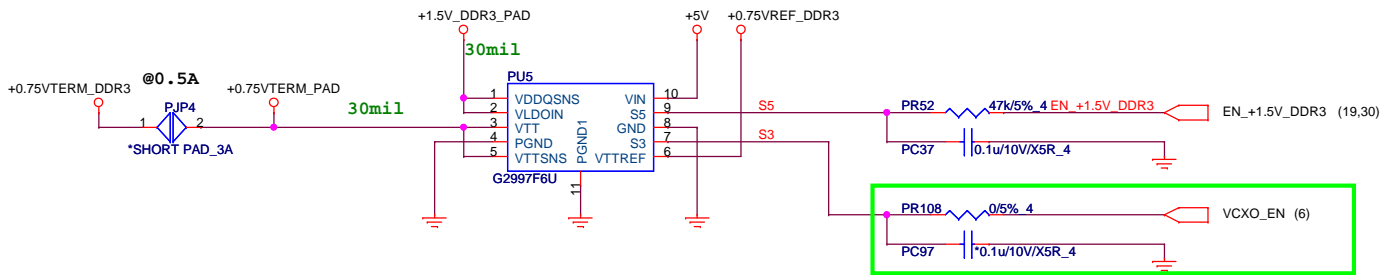
Quanta Computer Inc.
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Size	Document Number	Rev
	PWR (3/6) +1.8V+VCORE	4A
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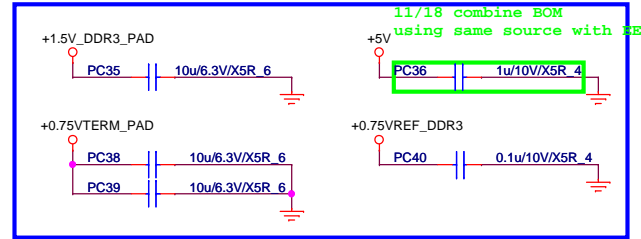
DDR3 POWER



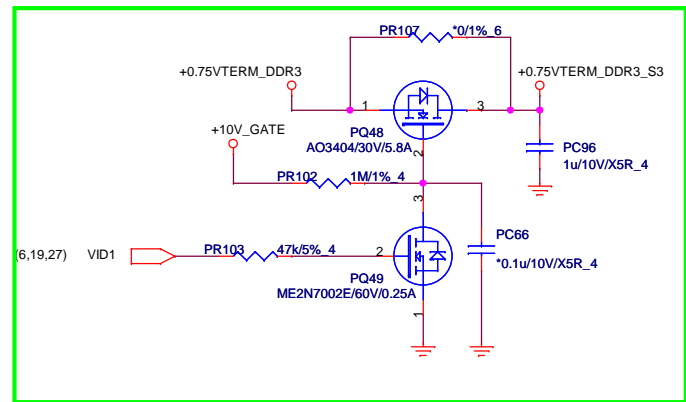
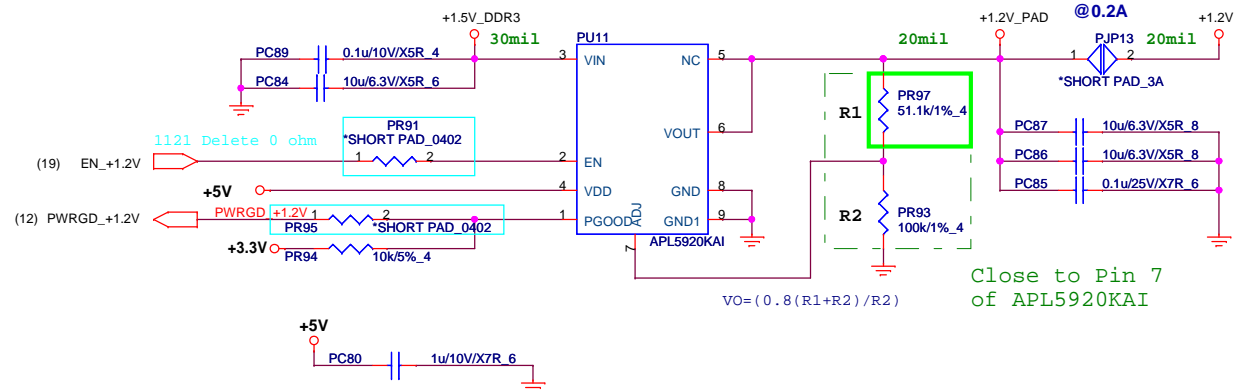
11/18 Modify BOM
Change 22uF FROM
PC27 to PC33



To Near PU18 PIN

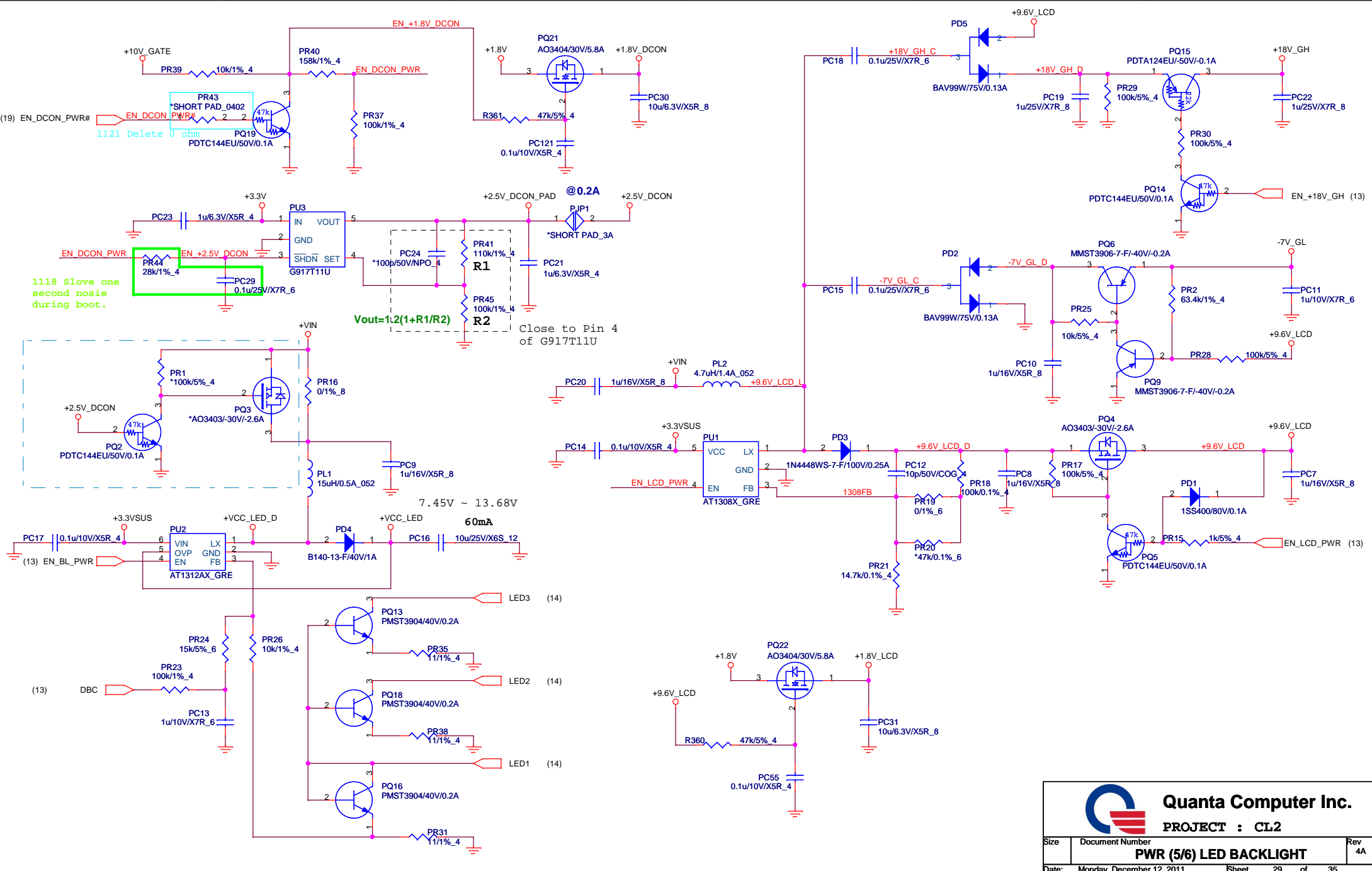



11/18 combine BOM with PR53, PR76
Change PR97 FROM 51k to 51.1k

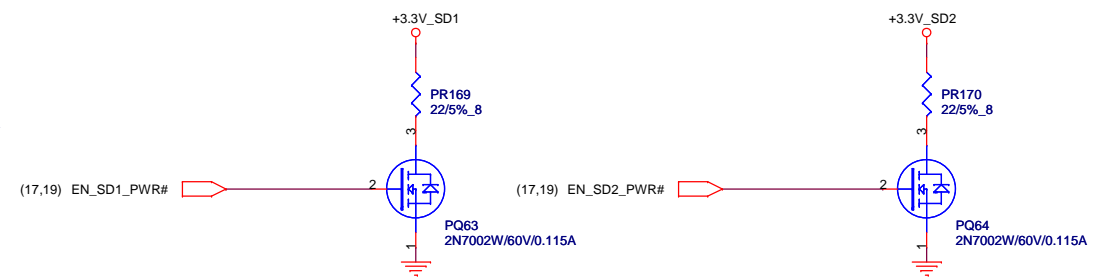
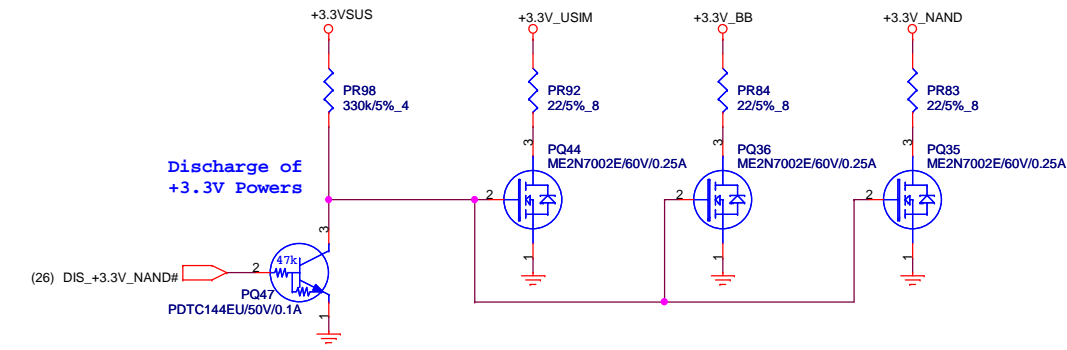
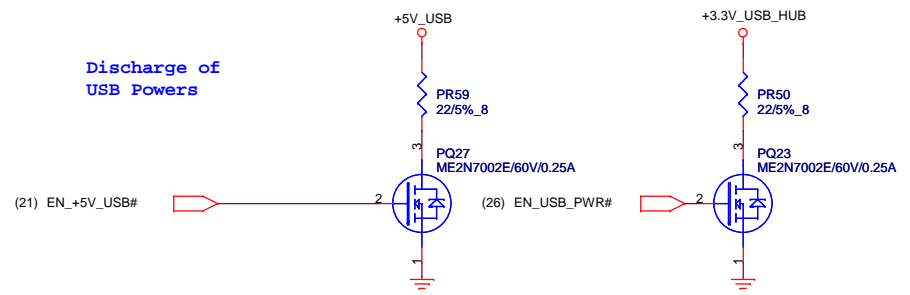
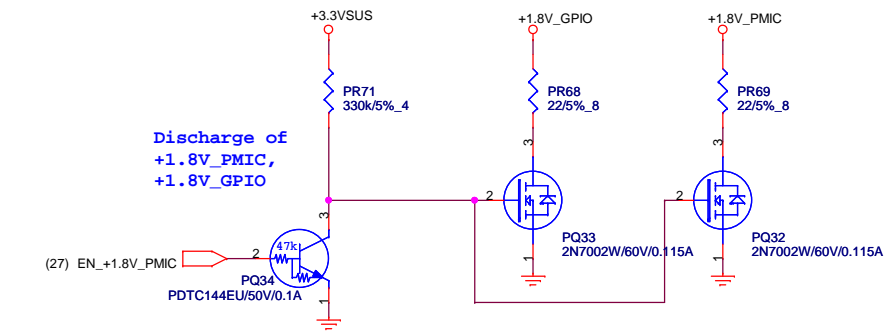
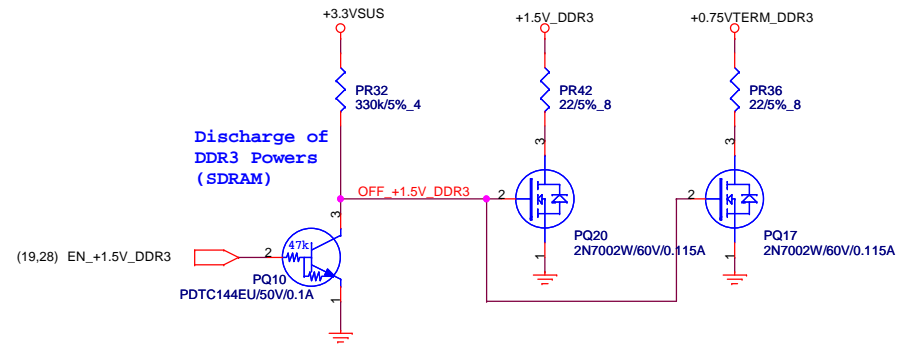
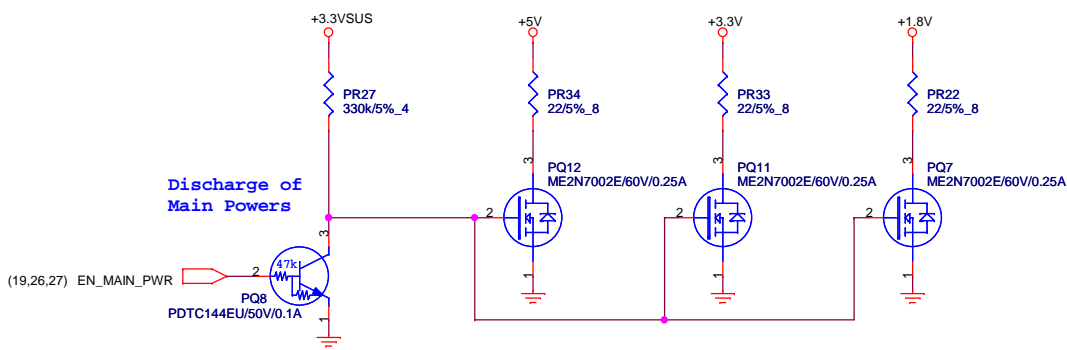



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	PWR (4/6) DDR3 PWR/+1.2V	4A
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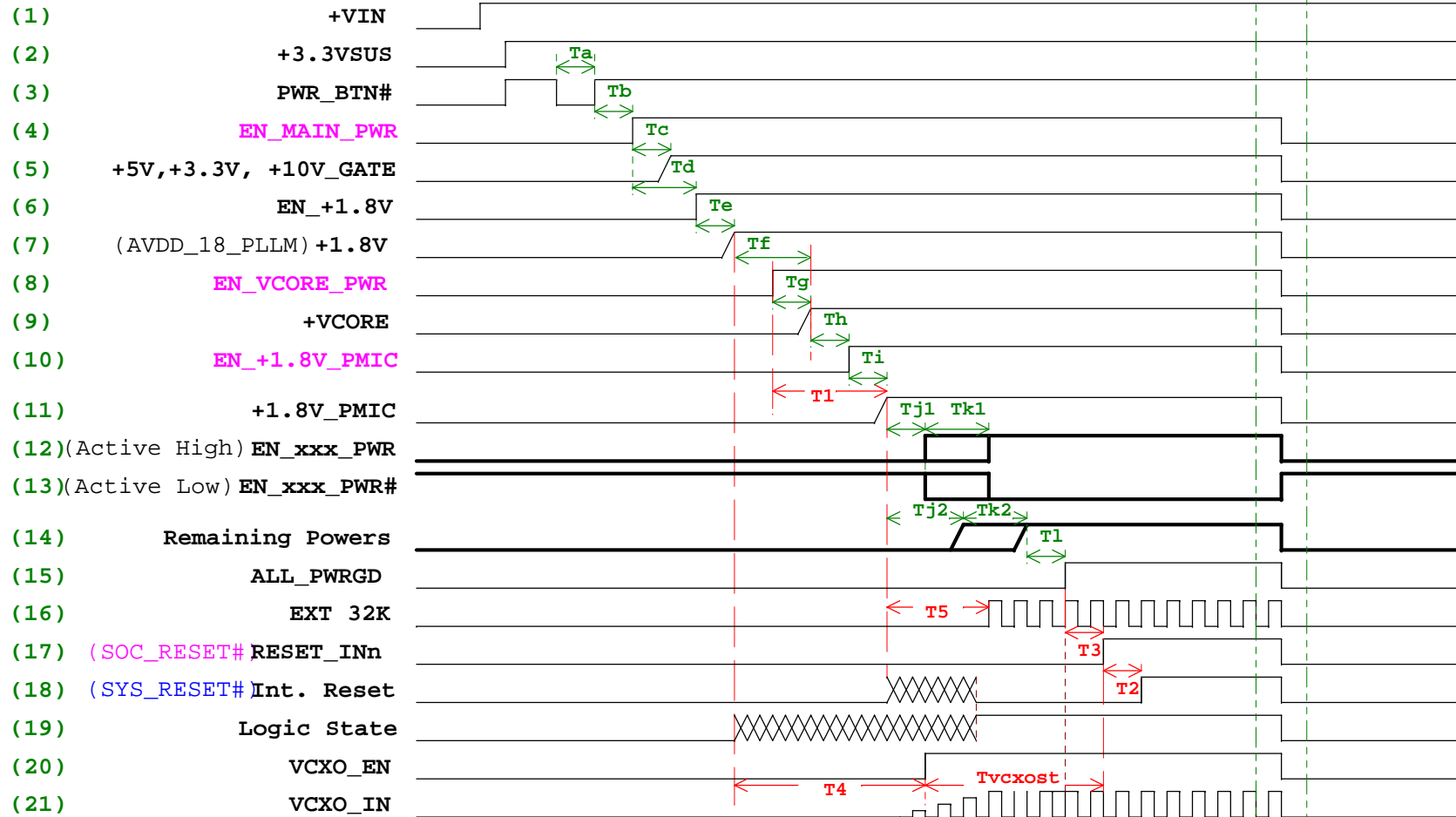
 Quanta Computer Inc. PROJECT : CL2		Rev 4A
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Power on sequence

OFF ?



Pink signals from EC
 Blue signals from SOC
 Black signals from others

T1 < 50us
 T2 < 100us
 T3 > 150us
 T4 > 0us
 T5 > 0us
 Tvcxost < ~7ms

 Quanta Computer Inc. PROJECT : CL2		Rev 4A
POWER SEQUENCE TIMING		
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Schematic modify Item and History :

A1-->A2

1. Design issue -

Page4

- a.Change U4 from 1338-31 to1338-18
- b.Change the VCC_RTC from +1.8_PMIC to +1.8V_GPIIO
- d.Delete some reserved resistor for 1338-31

Page5

- a.Add Game buttoun function on GPIO of Soc.
- b.Change the USB power control from Soc to EC.
- c.Change R94 from 0 ohm to 22 ohm ; R88 need to close to the CN7.
- d.Modify the GFGDATA0-5 to GFGDATA1-6.
- e.Add "Boot device selection".
- e.Modify the footprint of U10.
- f.populate R87 and depopulate D6.
- g.Change the GPIO_71/72 from EC_SCL/SDA to SOC_KBD_CLK/SDA.

Page6

- a.USB_ID pull down with R324.
- b.Change the p/n of Y3
- c.SOC_RESET# pull up with R272.
- d.Change the debug connector by OLPC and add CN23 for UART
- e.Pop R188 and depop R187.

Page7

- a.Add eMMC function on Soc
- b.Change the C90 form 0.1uf to 100pf

Page8

- a.depop R46.
- b.change the AVDD_USB from +3.3V_USB_HUB to +3.3_SOC
- b.change the VCC_IO_SDMMC from +3.3V_SD to +3.3V_SD2

Page11

- a.Swap the memory bus for layout's request.

Page12

- a.Change the +3.3VSUsto +3.3V_BB on Q4.2 for preventing the leakage of PWRGD_+3.3V_SOC.

Page13

- a.Change the p/n of Y1
- b.Modify the GFGDATA0-5 to GFGDATA1-6.

Page14

- a.Change the Audio IC from ALC5624 to ALC5631Q.
- b.Pop R36.

Page15

- a.change the pulled up voltage of R39 from +1.8V_AUDIO to +3.3V_SOC

- b.change the R179/R173 from 220ohm 0204 to 470ohm 0805.
- c.modify the schematic of internal microphone.
- d.Add CN24 for Tactile Feedback

Page17

- a.change Micro-SD socket to ext. SD.

Page18

- a.Change the NAND flash to eMMC.

Page19

- a.Change KB3930QF to IO3731.
- b.Add Reflash EC function/ REFLASH EC HEADER/ Boot rom selection
- c.Change EC_ID0 from A1 to A2 with using R268 30kohm.

Page20

- a.Pulling up the SDDA_CMD with R250 for WLAN function.

Page21

- a.Change 3.3V_USB_HUB to +3.3VA_USB_HUB on decoupling capactiors of IC.

Page22

- a.Change the camera power IC.
- b.Short between U5.20 and U5.24 : Short t between U5.19 and U5.25.
- c.add touch screen connector. (CN22)

Page23

- a.Adding 4 game buttons.

2.ESD/EMI-

- a.Reserve the ETV2 on BT1
- a.On page12, adding some capactiors for EMI's request.



Quanta Computer Inc.

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Schematic modify Item and History :

A2-->A3

1. Design issue -

Page4

a.Change R29 to 330 ohm, and C227 to 10 uF to fix #10999.


Page15

a.Change +3.3VA_AUDIO to +3.3V_AVDD everywhere it appears to fix #10974

2. Modify by supplier -

Page5

a. Change the Armada610 footprint by datasheet

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Schematic modify Item and History :

B -->C

1. HW Changes -

Page4

- a. Add Q16 2N7002W and change C227 from 10uf to 1uf, R29 from 330ohm to 47ohm to fix spec issue
- b. delete C224/C219 for time accuracy.

Page5

- a. Add MEM_GPIO0 and GPIO1 to verify memory size
- b. add pull-up resistors (R156/R158) on G_sensor I2C bus
- c. depopulate R159 for customer's request

Page6

- a. Add SELLP_IND for suspend/resume function
- b. Delete Mavrell's debug circuit.
- c. delete a reserved 32.768kHz circuit.

Page7

- a. Add CAM_PWRDN to control 2'nd source of camera

Page13

- a. Change L22/L23/L30/L28/L27 for EOD issue.
- b. Delete some 0 ohm.

Page15

- a. Delete 10uf C235/C215/C167, and change C232/C230 to 2.2uf to meet the latest schematic
- b. Change C307 to 1uf

Page16

- a. Add R387/R388 2k to meet the latest schematic
- b. delete Tactile Feedback circuit

Page17

- a. Modify the ext. SD (add a pin to connect shideing)
- b. Add RC delay circuit and Change C279/C310 from 10uf to 4.7uf to avoid inrush current
- c. Using push-push type (CN26) and hinge type(CN25) to repace original ext. SD.

Page18

- a. modify the bypass capactiors on +3.3V_eMMC_VCC/ and +3.3V_eMMC_IO

Page19

- a. Modify the LV_SET, PWR_LMT_ON#, LED_INHIBIT# and OLS_CATHODE
- b. add a 2.2uf on EC_RST# to fix boot issue
- c. Change the EC_ID table
- d. add a suspend./resurme circuit

- e. delete another OLS circuit.

Page20

- a. Change the controlled OLS_LED circuit from WLAN_LED to sotrage LED

Page21

- a. modify the size of C269

Page22

- a. Change the U15 because EOD issue
- b. depopulate R217 for 2'nd source.

Page22

- a. add a RC delay ciucuit for inrush current
- b. Change the controlled OLS_LED circuit from WLAN_LED to sotrage LED

2. Power changes -

Page25

- a. fine tune some components' values.
- b. change PD31 for cumsomer's request

Page26

- a. add a 22uf on +3.3V

Page27

- a. pull-up a resistor 10k on VID_1
- b. connect EN_+1.8V_GPIO to Q16.2



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Schematic modify Item and History :

C --> D

1. HW Changes -

All Page

1. Delete some 0 ohm.

Page5

1. Fine tune the damping resistor for the signal quality of ext.SD and int.SD.

Page15

1. Add a RC filter circuit, and close to Audio codec for speaker noise
2. Fine tune the trace spacing of speak out

Page16

1. Change TVS on Headphone for ESD
2. Add a 100pf on int. MIC and two ground shedding on ext. SD for EMI

Page17

1. Modify the position of reserved int. SD(CN25) for shorting issue between CN25 and CN26.

Page23

1. Using dual-LEDs to instead of signal-LED on OLS
2. Populate R351~R354 for dropping key issue after resume.

2. Power changes -

All Page

1. Delete some 0 ohm.

Page28

1. Add a power control of DDR3 termination for reducing the power consumption during suspend.

Page25

1. Change ISL9519_GND to GND on ACOK of charge IC.



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