

## EC Pinout

KB3700A1				CL1/CL1A			CL1B		
Pin Name	Pin	IRQ ?	Special ?	Signal Name	Dir.	Description	Signal Name	Dir.	Description
SPIDI	58			SPIDI	I	SPI ROM I/F	SPIDI	I	SPI ROM I/F
SPIDO	59			SPIDO	O	SPI ROM I/F	SPIDO	O	SPI ROM I/F
SPICLK	60			SPICLK	I	SPI ROM I/F	SPICLK	I	SPI ROM I/F
SPICS#	61			SPICS#	I	SPI ROM I/F	SPICS#	I	SPI ROM I/F
SERIRQ	5			LSERIRQ	O	LPC Interrupt	LSERIRQ	O	LPC Interrupt
LFRAME#	7			LFRAME#	I/O	LPC Frame	LFRAME#	I/O	LPC Frame
LAD0	14			LAD0	I	LPC Address/Data	LAD0	I	LPC Address/Data
LAD1	12			LAD1	I	LPC Address/Data	LAD1	I	LPC Address/Data
LAD2	11			LAD2	I	LPC Address/Data	LAD2	I	LPC Address/Data
LAD3	8			LAD3	I	LPC Address/Data	LAD3	I	LPC Address/Data
GPIO 03	16		PCICLK	CLK_EC_LPC	I	LPC Clock	CLK_EC_LPC	I	LPC Clock
GPIO 04	21		PCIRST	PCI_RST#	I	LPC Reset	PCI_RST#	I	LPC Reset
GPIO 00	6	x		VR_ON#	O	PWR ctl	KEY_IN_1	I	Button I/F
GPIO 01	9	x		WLAN_EN	O	PWR ctl	KEY_IN_2	I	Button I/F
GPIO 02	10	x		SWI#	O	System Interrupt	KEY_IN_3	I	Button I/F
GPIO 05	22	x	SCI	SCI#	O	System Interrupt	SCI#	O	System Interrupt
GPIO 06	23	x	PWMA	TX	O	EC Serial / ISP I/F Tx	TX	O	EC Serial Tx/D
GPIO 07	24	x	ISP_RX	RX	I	EC Serial / ISP I/F Rx	-	I	-
	24		PWMB	BAT_L0	O	WLAN I/F	CHGCTL	O	DC Input current Ctl
GPIO 08	25	x	PWMC	ACIN	I	DC Input Pwr good	LED_CHG_G#	O	LED ctl
GPIO 09	26	x	PWMC	LED_PWR#	O	LED ctl	LED_PWR#	O	LED ctl
GPIO 0A	27	x	PWMC	LED_CHG_R#	O	LED ctl	LED_CHG_R#	O	LED ctl
GPIO 0B	28	x		WAKEUP	I	Button I/F	AC_IN	I	DC Input Pwr good
GPIO 0C	29	x		PWR_BUT#	O	PWR ctl	LID_SW	I	reserved for Lid Switch
GPIO 0D	30	x	ECRST	ECRST#	I	EC Reset	ECRST#	I	EC Reset
GPIO 0E	31	x		CHG	O	Batt. Charge Control	CHG	O	Batt. Charge Control
GPIO 0F	32	x		CC0	O	Batt. Charge CC mode	CC0	O	Batt. Charge CC mode
GPIO 10	37	x		BAT_L2	O	WLAN I/F	MSD_PWROFF	O	Internal SD power control
GPIO 11	43	x		KBCLK	O	PS2 Keybd I/F	KBCLK	O	PS2 Keybd I/F
GPIO 12	44	x		KBSDAT	I/O	PS2 Keybd I/F	KBSDAT	I/O	PS2 Keybd I/F
GPIO 13	45	x		TPCLK	O	PS2 Mouse I/F	TPCLK	O	PS2 Mouse I/F
GPIO 14	46	x		TPDAT	I/O	PS2 Mouse I/F	TPDAT	I/O	PS2 Mouse I/F
GPIO 15	47			KEY_OUT_3	O	Button I/F	KEY_OUT_3	O	Button I/F
	47	x	ISP_CLK	ISP_CLK	I	ISP I/F clock	-	I	-
GPIO 16	48			KEY_OUT_1	O	Button I/F	KEY_OUT_1	O	Button I/F
	48	x	ISP_EN	ISP_EN#	I	ISP I/F enable	-	I	-
GPIO 17	53	x		KEY_OUT_2	O	Button I/F	KEY_OUT_2	O	Button I/F
GPIO 18	54	x		WAKE_PRECHG	O	Batt. Charge trickle	POWER_BUTTON#	I	Button I/F - power
GPIO 19	55	x		EB_MODE	I	EB switch sense	VIN_OK	I	VIN voltage below limits
GPIO 1A	56	x		MAIN_ON	I/O	PWR ctl	MAIN_ON	I	PWR ctl - status

## EC Pinout

KB3700A1				CL1/CL1A			CL1B		
Pin Name	Pin	IRQ ?	Special ?	Signal Name	Dir.	Description	Signal Name	Dir.	Description
GPIO 1B	57	x		PWR_BUT_in	I	Lid switch sense	SUSC#	I	PWR ctl (indicates S4/S5)
GPIAD 0	38		A/D	TEMP	I	Temperature Sense	CHGCUR	I	DC Input current sense
GPIAD 1	39		A/D	EC_ID	I	ID Sense	EC_ID0	I	ID Sense
GPIAD 2	40		A/D		I	available	EC_ID1	I	ID Sense
GPIAD 3	34		A/D	SUS_ON	I	PWR ctl	CHGVOL	I	Main Rail Voltage sense
GPIAD 4	35		A/D	CVM	I	Batt. Charge CV mode	CVM	I	Batt. Charge CV mode
GPIAD 5	36		A/D	WAKEUP_EC	I	WLAN I/F	M_VA2	I	Voltage at DC Line In
GPIO E0	1			EC_WP#	O	SPI ROM I/F	EC_IRQ#	O	EC to host interrupt
GPIO E1	2			CV_SET	O	Batt. Charge V mode	CV_SET	O	Batt. Charge V mode
GPIO E2	3			DQ	I/O	Batt. Charge One-wire	DQ	I/O	Batt. Charge One-wire
GPIO E3	4			LED_CHG_G#	O	LED ctl	SYSOK	I	PWR ctl - status
GPIO E4	17			KEY_IN_1	I	Button I/F	PWRGD	I	PWR ctl - status
GPIO E5	18			KEY_IN_2	I	Button I/F	VR_ON	O	PWR ctl
GPIO E6	19			KEY_IN_3	I	Button I/F	PWR_BUT_OUT#	O	PWR ctl
GPIO E7	20			POWER_BUTTON#	I	Button I/F - power	TRICKLE_CHG	O	Batt. Charge trickle
GPIO E8	33			BAT_L1	O	WLAN I/F	KBD_PWREN#	O	PWR ctl
GPIO EC	49			SPIWP#	O	SPI ROM I/F	TCM_CLK	O	I2C EEPROM
GPIO ED	50			DCON_EN	O	PWR ctl	DCON_EN	O	PWR ctl
GPIO EE	51			WLAN_RESET#	O	WLAN I/F	RSM_RST#	O	PWR ctl (VX855 RSMRST)
GPIO EF	52			ECPWRRQST	O	DCON I/F	TCM_DAT	I/O	I2C EEPROM

Red text indicates a signal whose transitions are an event for which the EC would want to wake out of sleep.  
Blue indicates new since v12