

| Pin Name | Pin | Int | Special      | GPIO   | Updated Signal Name (2010/8/4) | Dir | Domain       | Description                         | Notice              |
|----------|-----|-----|--------------|--------|--------------------------------|-----|--------------|-------------------------------------|---------------------|
| GA20     | 1   |     | GA20         | GPIO00 | EN_VCORE_PWR                   | 0   | BQCC04HIV    | Enable VCORE                        |                     |
| KBCRST#  | 2   |     | KBCRST#      | GPIO01 | Testing point                  | 0   | BQCC04HIV    | Open                                |                     |
| GPIO04   | 6   |     |              | GPIO04 | EN_+1.8V_GPIO#                 | 0   | BQCC04HIV    | Enable the +1.8V_GPIO               |                     |
| PCRST#   | 13  |     |              | GPIO05 | Testing point                  |     | BQC16H1      | Open                                |                     |
| GPIO07   | 14  |     |              | GPIO07 | Testing point                  |     | BQCC04HIV    | Avoid                               |                     |
| GPIO08   | 15  |     | i_clk_8051   | GPIO08 | Testing point                  |     | BQCC04HIV    | Avoid                               |                     |
| GPIO08   | 16  |     | i_clk_peri   | GPIO08 | Testing point                  |     | BQCC04HIV    | Avoid                               |                     |
| GPIO0A   | 17  |     | OWM1         | GPIO0A | CHRG_DQ                        | I/O | BQCC04HIV    | 1-Wire                              |                     |
| GPIO0B   | 17  |     | ESB_CLK      | GPIO0B | EN_+3.3V_NAND#                 | 0   | BQCCW16HIV   | Enable the +3.0V                    |                     |
| GPIO0C   | 18  |     | ESB_DAT      | GPIO0C | Testing point                  | 0   | BQCC08HIV    | Enable the +3.0V_SDMMC, +3.0V_BB    |                     |
| GPIO0D   | 19  |     | RLC_TX2      | GPIO0D | EN_+1.5V_DDR3                  | 0   | BQCC04HIV    | Enable the +1.5V_DDR3               |                     |
| SC#      | 20  |     | SC#          | GPIO0E | Testing point                  | 0   | BQCC04HIV    | Open                                |                     |
| PWM0     | 21  |     | PWM0         | GPIO0F | LED_BAT_G                      | 0   | ??BQC16HIV?? | Battery LED Green                   | Typo in datasheet?  |
| VCC      | 22  |     |              |        |                                |     |              |                                     |                     |
| PWM1     | 23  |     | PWM1         | GPIO10 | LED_BAT_R                      | 0   |              | Battery LED Red                     |                     |
| GND      | 24  |     |              |        |                                |     |              |                                     |                     |
| GPIO11   | 25  |     | PWM2         | GPIO11 | CHRG_BAT                       | 0   |              | Battery LED Red                     |                     |
| FANPWM0  | 26  |     | FANPWM0      | GPIO12 | LED_PWR                        | 0   |              | PWM signal to charge mosfet         |                     |
| FANPWM1  | 27  |     | FANPWM1      | GPIO13 | Testing point                  | 0   |              | Power LED                           |                     |
| FANFB0   | 28  |     |              | GPIO14 | Testing point                  |     |              | Open                                |                     |
| FANFB1   | 29  |     |              | GPIO15 | Testing point                  |     |              | Open                                |                     |
| E51TXD   | 30  |     |              | GPIO16 | EC_TX                          | 0   |              | UART Out                            |                     |
| E51RXD   | 31  |     |              | GPIO17 | EC_RX                          | 0   |              | UART In                             |                     |
| GPIO18   | 32  |     |              | GPIO18 | EC_IRQ#                        | 0   |              | EC wants to talk to host            |                     |
| GPIO19   | 34  |     | PWM3         | GPIO19 | CHRG_VOL_CTL                   | 0   |              | DC Input power control              |                     |
| GPIO1A   | 36  |     | NUMLED#      | GPIO1A | EN_DCON_PWR                    | 0   |              | Enable +2.5 for dcon                |                     |
| ECRST#   | 37  |     |              |        | EC_RST#                        | 0   |              | EC Reset                            |                     |
| CLKRUN#  | 38  |     | CLKRUN#      | GPIO1D | Testing point                  | 0   |              | Testing point                       |                     |
| KSO0     | 39  |     | TP_TEST      |        | Testing point                  |     |              | Avoid                               |                     |
| KSO1     | 40  |     | TP_PLL       |        | Testing point                  |     |              | Avoid                               |                     |
| KSO2     | 41  |     | TP_TMUX      |        | Testing point                  |     |              | Avoid                               |                     |
| KSO3     | 42  |     | TP_ISP       |        | Testing point                  |     |              | Avoid                               |                     |
| KSO4     | 43  |     | KSO4         | GPIO24 | CHRG_CVM                       | 0   |              | CC/CV indication from charger       | Possibly going away |
| KSO5     | 44  |     | KSO5/PC/CLK  | GPIO25 | Testing point                  | 0   |              | RST_CORE will reset the +VCC_MAIN?? | Need confirm        |
| KSO6     | 45  |     | KSO6/PCRST#  | GPIO26 | Testing point                  |     |              |                                     |                     |
| KSO7     | 46  |     | KSO7/SERIRQ  | GPIO27 | Testing point                  |     |              |                                     |                     |
| KSO8     | 47  |     | KSO8/LFRAME# | GPIO28 | Testing point                  |     |              |                                     |                     |
| KSO9     | 48  |     | KSO9         | GPIO29 | CHRG_VOL_SEL                   | 0   |              | Charge voltage selection            |                     |
| KSO10    | 49  |     | KSO10        | GPIO2A | PWR_BTN#                       | 0   |              | Input from power button             |                     |
| KSO11    | 50  |     | LAD3/KSO11   | GPIO2B | Testing point                  |     |              |                                     |                     |
| KSO12    | 51  |     | LAD2/KSO12   | GPIO2C | Testing point                  |     |              |                                     |                     |
| KSO13    | 52  |     | LAD1/KSO13   | GPIO2D | Testing point                  |     |              |                                     |                     |
| KSO14    | 53  |     | LAD0/KSO14   | GPIO2E | Testing point                  |     |              |                                     |                     |
| KSO15    | 54  |     | KSO15        | GPIO2F | Testing point                  |     |              |                                     |                     |
| KSO10    | 55  |     | KSO10        | GPIO30 | CHRG_TRICKLE                   | 0   |              | Enable the +3.3V_SOC                | PWR_BUT_OUT unused  |
| KSI1     | 56  |     | KSI1         | GPIO31 | EN_+3.3V_SOC#                  | 0   |              | Needed?                             |                     |
| KSI2     | 57  |     | KSI2         | GPIO32 | EN_KBD_PWR#                    | 0   |              | Avoid                               |                     |
| KSI3     | 58  |     | KSI3         | GPIO33 | Testing point                  | 0   |              | Avoid                               |                     |
| KSI4     | 59  |     | EDL_CS       |        | Testing point                  |     |              | Avoid                               |                     |
| KSI5     | 60  |     | EDL_CLK      |        | Testing point                  |     |              | Avoid                               |                     |
| KSI6     | 61  |     | EDL_DIN      |        | Testing point                  |     |              | Avoid                               |                     |
| KSI7     | 62  |     | EDL_DO       |        | Testing point                  |     |              | Avoid                               |                     |
| AD0      | 63  |     | AD0          | GPIO38 | EC_ID0                         | 0   |              | Board ID 0                          |                     |

|        |     |                     |        |                |        |                                    |            |
|--------|-----|---------------------|--------|----------------|--------|------------------------------------|------------|
| AD1    | 64  | AD1                 | AD1    | GPIO39         | EC_ID1 | 1                                  | Board ID 1 |
| AD2    | 65  | AD2                 | GPIO3A | CHRG_AC_IN     | 1      | Voltage at DC Line In              |            |
| AD3    | 66  | AD3                 | GPIO3B | Testing point  | 1      | Current at DC Line In              |            |
| DA0    | 68  | DA0                 | GPO3C  | Testing point  |        |                                    |            |
| DA1    | 70  | DA1                 | GPO3D  | Testing point  |        |                                    |            |
| DA2    | 71  | DA2                 | GPO3E  | Testing point  |        |                                    |            |
| DA3    | 72  | DA3                 | GPO3F  | Testing point  |        |                                    |            |
| GPIO40 | 73  | CIR_RX              | GPIO40 | CHRG_AC_OK     | 1      | External power applied (Old AC_IN) |            |
| GPIO41 | 74  | CIR_RLC_TX / PECL   | GPIO41 | EN_MAIN_PWR    | 0      | Main rail current sense            |            |
| AD4    | 75  | AD4                 | GPIO42 | CHRG_CUR_SNS   | 1      | Main rail voltage sense            |            |
| AD5    | 76  | AD5                 | GPIO43 | CHRG_VOL_SNS   | 1      | ??                                 |            |
| SCL0   | 77  | SCL0                | GPIO44 | EC_SCL         |        | ??                                 |            |
| SDA0   | 78  | SDA0                | GPIO45 | EC_SDA         |        | open                               |            |
| SCL1   | 79  | SCL1                | GPIO46 | Testing point  |        | open                               |            |
| SDA1   | 80  | SDA1                | GPIO47 | Testing point  |        | open                               |            |
| KSO16  | 81  | KSO16               | GPIO48 | SOC_RESET#     | 1      |                                    |            |
| KSO17  | 82  | KSO17               | GPIO49 | SYS_RESET#     | 0      |                                    |            |
| PSCLK1 | 83  | PSCLK1/ SCL2        |        | KBD_CLK        | I/O    |                                    |            |
| PSDAT1 | 84  | PSDAT1/ SDA2        |        | KBD_DAT        | I/O    |                                    |            |
| PSCLK2 | 85  | PSCLK2/ SCL3        |        | TPD_CLK        | I/O    |                                    |            |
| PSCLK3 | 86  | PSDAT2/ SDA3        |        | TPD_DAT        | I/O    |                                    |            |
| PSDAT3 | 87  | PSCLK3              | GPIO4E | Testing point  |        | Open                               |            |
| GPIO50 | 88  | PSDAT3              | GPIO4F | Testing point  | 0      | Open                               |            |
| GPIO52 | 89  | GPIO50              | GPIO50 | EN +1.2V       |        | Open the +1.2V                     |            |
| GPIO53 | 90  | E51CS#              | GPIO52 | Testing point  |        | Open                               |            |
| GPIO54 | 91  | CAP_SLED# / E51TMR1 | GPIO53 | Testing point  |        | Open                               |            |
| GPIO55 | 92  | WDT_LED# / E51TMR0  | GPIO54 | Testing point  |        | Open                               |            |
| GPIO56 | 93  | SCROLED# / E51N10   | GPIO55 | EC_SPL_CMD     | 1      | EC ↔ Host Communication            |            |
| GPIO56 | 95  | E51N11              | GPIO56 | EC_SPL_ACK     | 0      | EC ↔ Host Communication            |            |
| GPIO56 | 97  | N                   |        | SDI_CS#        | 1      | EC ↔ Host Communication            |            |
| GPIO56 | 98  | N                   |        | SDI_CLK        | 1      | EC ↔ Host Communication            |            |
| GPIO56 | 99  | N                   |        | SDI_MOSI       | 1      | EC ↔ Host Communication            |            |
| GPIO56 | 99  | N                   |        | SDI_MISO       | 1      | EC ↔ Host Communication            |            |
| GPIO56 | 100 | N                   |        | Testing point  |        | SPI flash                          |            |
| GPIO56 | 101 | N                   |        | EC_SPL_MISO    | 1      | SPI flash                          |            |
| GPIO56 | 101 | N                   |        | EC_SPL_MOSI    | 0      | SPI flash                          |            |
| GPIO56 | 101 | N                   |        | ALL_PWRRGD     | 0      | Enable the +1.8V                   |            |
| GPIO56 | 102 | N                   |        | EC_SPL_CLK     | 0      | SPI flash                          |            |
| GPIO56 | 102 | N                   |        | EN +1.8V_PMIC# | 0      | Enable the +1.8V_PMIC              |            |
| GPIO56 | 103 | N                   |        | EC_SPL_CS#     | 0      | SPI flash                          |            |
| GPIO56 | 103 | N                   |        | Testing point  |        |                                    |            |
| GPIO56 | 104 | N                   |        | Testing point  |        |                                    |            |
| GPIO56 | 104 | N                   |        | Testing point  |        |                                    |            |
| GPIO56 | 105 | N                   |        | Testing point  |        |                                    |            |
| GPIO56 | 106 | N                   |        | Testing point  |        |                                    |            |
| GPIO56 | 107 | N                   |        | Testing point  |        |                                    |            |
| GPIO56 | 108 | N                   |        | Testing point  |        |                                    |            |
| GPIO56 | 109 | N                   |        | Testing point  |        |                                    |            |
| GPIO56 | 110 | N                   |        | Testing point  |        |                                    |            |
| GPIO56 | 110 | N                   |        | Testing point  |        |                                    |            |
| GPIO56 | 119 | N                   |        | Testing point  |        |                                    |            |
| GPIO56 | 120 | N                   |        | Testing point  |        |                                    |            |
| GPIO56 | 121 | N                   |        | Testing point  |        |                                    |            |
| GPIO56 | 126 | N                   |        | Testing point  |        |                                    |            |
| GPIO56 | 127 | N                   |        | Testing point  |        |                                    |            |
| GPIO56 | 128 | N                   |        | Testing point  |        |                                    |            |

Signals to stay away from for GPIO because of boot strapping features or SPI programming

|      |    |          |
|------|----|----------|
| KSO0 | 39 | TP_TEST  |
| KSO1 | 40 | TP_PLL   |
| KSO2 | 41 | TP_TMUUX |

|      |    |         |
|------|----|---------|
| KSO3 | 42 | TP_ISP  |
| KS14 | 59 | EDI_CS  |
| KS15 | 60 | EDI_CLK |
| KS16 | 61 | EDI_DIN |
| KS17 | 62 | EDI_DO  |

Domain Keys

|           |  |
|-----------|--|
| BQC04HIV  | Schmitt trigger; 04mA Output / Sink Current, Input / Output / Pull Up Enable(40K $\Omega$ ), 5 V Tolerance |
| BQCZ16HIV | Schmitt trigger; 16mA Output / Sink Current, Input / Output / Pull Up Enable(40K $\Omega$ ), 5 V Tolerance |
| BCC16HI   | 16mA Output / Sink Current, 5 V Tolerance, Input / Output Enable   |
| BQCW16HIV | Schmitt trigger; 16mA Output / Sink Current, 5 V Tolerance, Input / Output / Pull Up Enable                |
| BQC08HIV  | Schmitt trigger; 8mA Output / Sink Current, 5V Tolerance, Input / Output / Pull Up Enable                  |